

FIG. 2

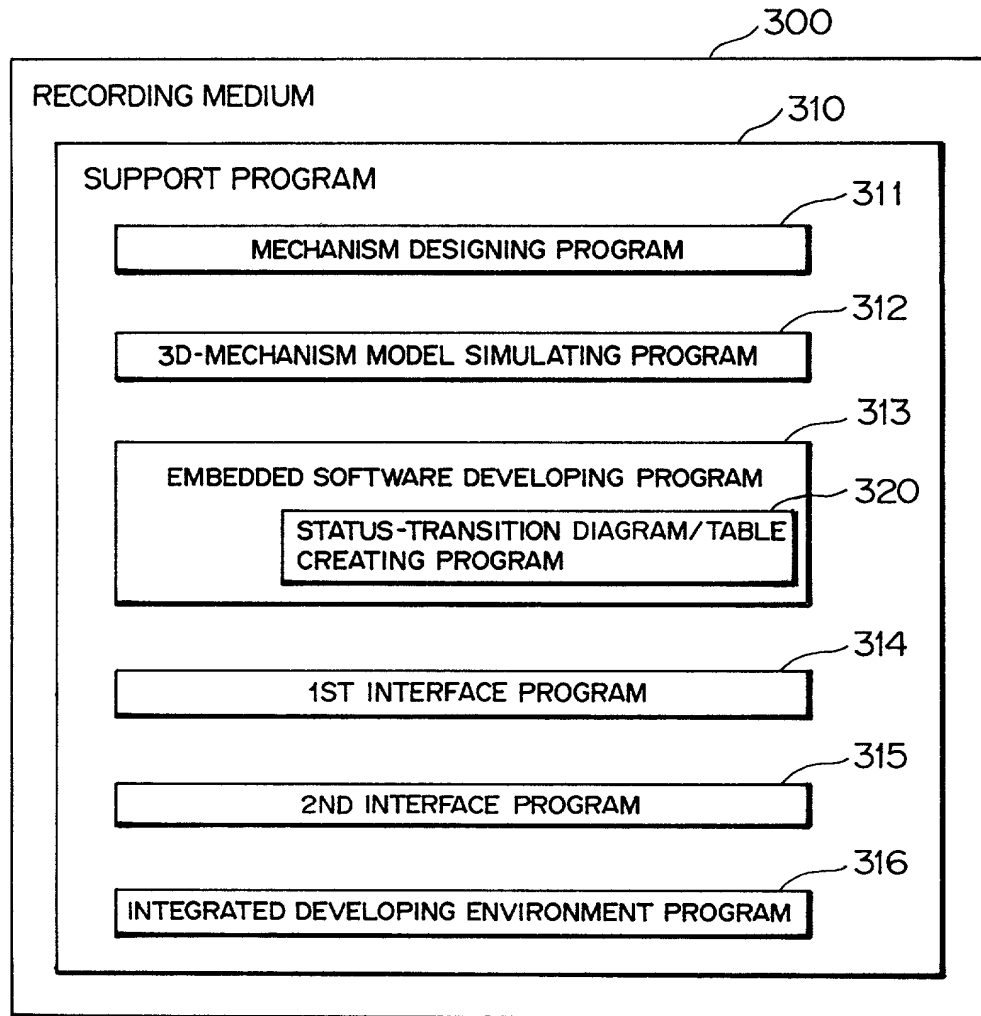


FIG. 3

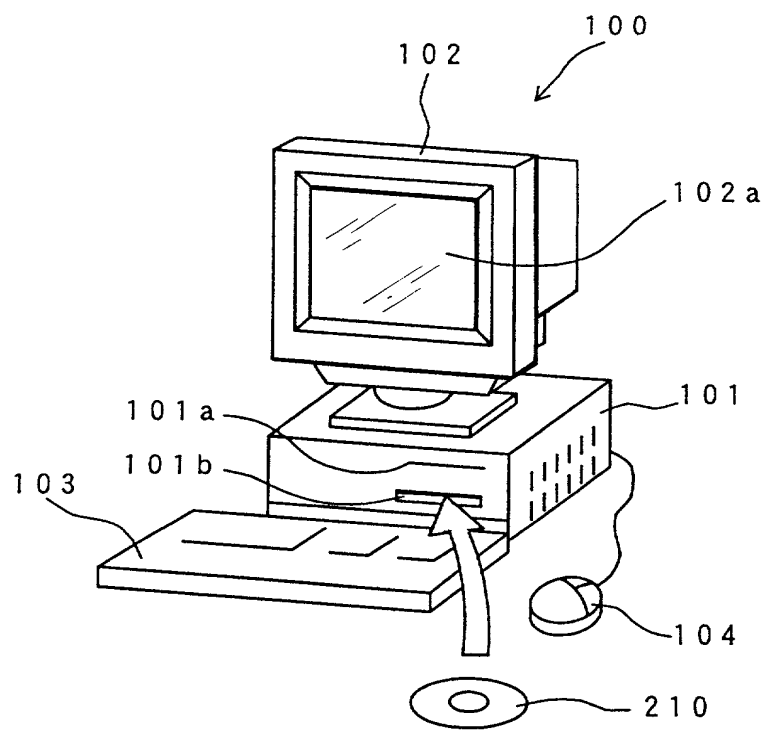


FIG. 4

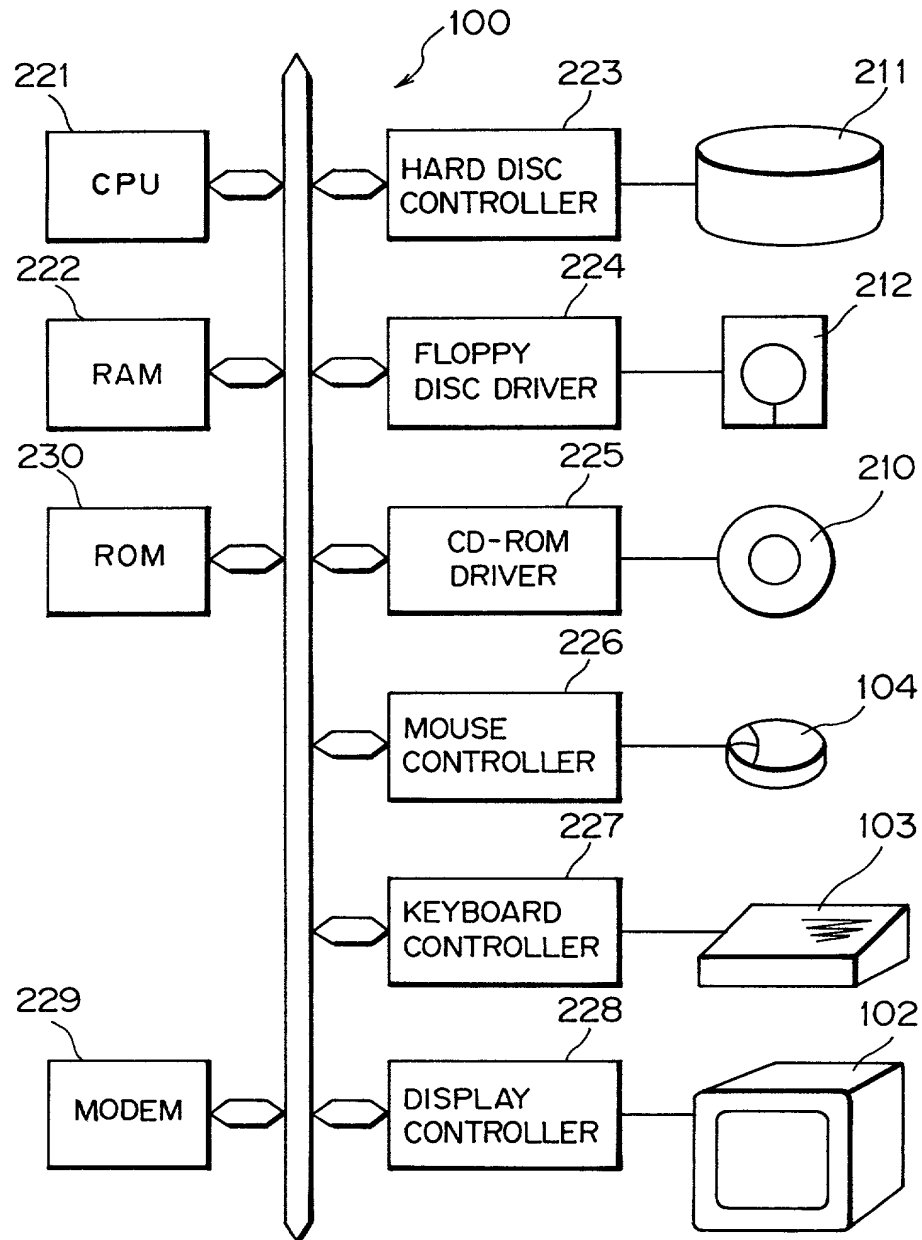


FIG. 5

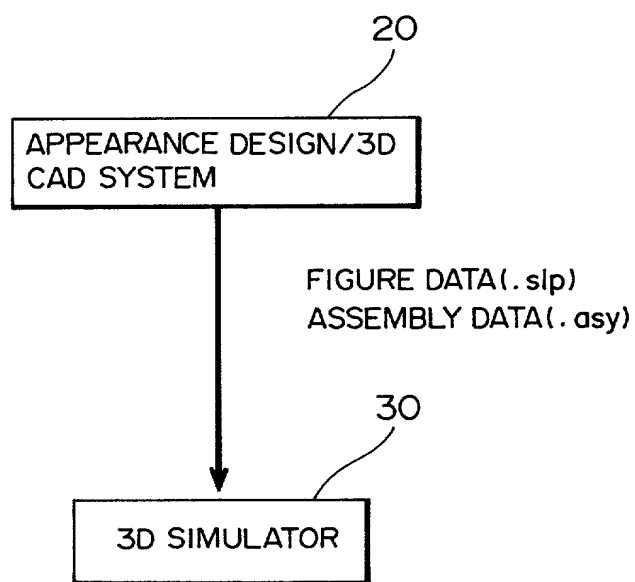


FIG. 6A

```

SOLID KAM_VERSION2_0_LIBRARY1
  COLOR 1. 000000 0. 660000 0. 000000
  FACET
    NORMAL 0. 000000 0. 952236 -0. 305364
    NORMAL 0. 000000 0. 952236 -0. 305364
    NORMAL 0. 000000 0. 952236 -0. 305364
  OUTER LOOP
    VERTEX 46. 667969 -54. 467480 -31. 538586
    VERTEX 46. 667969 -57. 256718 -40. 236450
    VERTEX 16. 667969 -57. 256718 -40. 236450
  ENDLLOOP
ENDFACET
FACET
  NORMAL 0. 000000 0. 952236 -0. 305364
  NORMAL 0. 000000 0. 952236 -0. 305364
  NORMAL 0. 000000 0. 952236 -0. 305364
  OUTER LOOP
    VERTEX 16. 667969 -57. 256718 -40. 236450
    VERTEX 16. 667969 -54. 467480 -31. 538586
    VERTEX 46. 667969 -54. 467480 -31. 538586
  ENDLLOOP
ENDFACET
.
.
.
FACET
  NORMAL 0. 000000 - 0. 472754 0. 881194
  NORMAL 0. 000000 - 0. 472754 0. 881194
  NORMAL 0. 000000 - 0. 472754 0. 881194
  OUTER LOOP
    VERTEX 91. 667969 -77. 803757 -26. 071329
    VERTEX 91. 667969 -73. 224937 -23. 614828
    VERTEX 46. 667969 -73. 224937 -23. 614828
  ENDLLOOP
ENDFACET
ENDSOLID KAM_VERSION2_0_LOBRARY1

```

FIG. 6B

```

BEGIN SPACE
# NO 1
  PARENT 0 # ROOT
  JOINTTYPE FREE
END

BEGIN "SEAT_ASSY_0_LIBRARY1"
# NO 2
  PARENT 1 # SPACE
  RELPOSITION 0. 000000 0. 000000 0. 000000
  RELATTITUDE 1. 000000 0. 000000 0. 000000 0. 000000
  1. 000000 0. 000000 0. 000000 0. 000000 1. 000000
  JOINTTYPE FIXED
END

BEGIN "SEAT_PAN_0_LIBRARY1"
# NO 3
  PARENT 2 # SEAT_ASSY_0_LIBRARY1
  SHAPE SEAT_PAN_0_LIBRARY1. SLP
  RELPOSITION 1. 810953 51. 404652 450. 053009
  RELATTITUDE 1. 000000 0. 000000 0. 000000 0. 000000
  1. 000000 0. 000000 0. 000000 0. 000000 1. 000000
  JOINTTYPE FIXED
END

BEGIN "KAM_VERSION2_0_LIBRARY1"
# NO 4
  PARENT 2 # SEAT_ASSY_0_LIBRARY1
  SHAPE KAM_VERSION2_0_LIBRARY1. SLP
  RELPOSITION 201. 712204 -211. 475098 811. 809509
  RELATTITUDE 0. 104385 0. 034423 -0. 993941 0. 994522
  -0. 009110 0. 104131 -0. 005471 -0. 999366 -0. 035185
  JOINTTYPE FIXED
END
.
.
.

```

FIG. 7

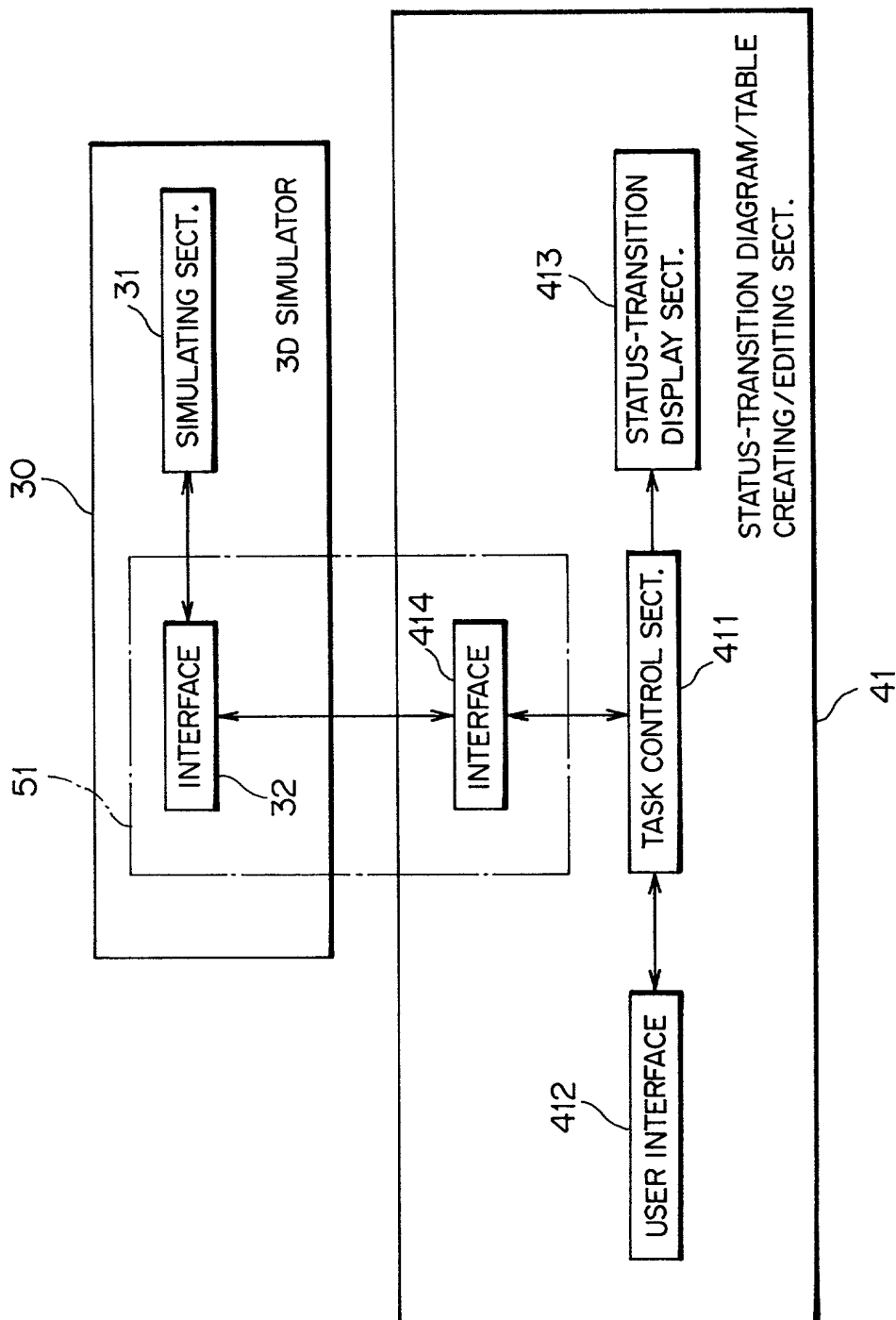


FIG. 8

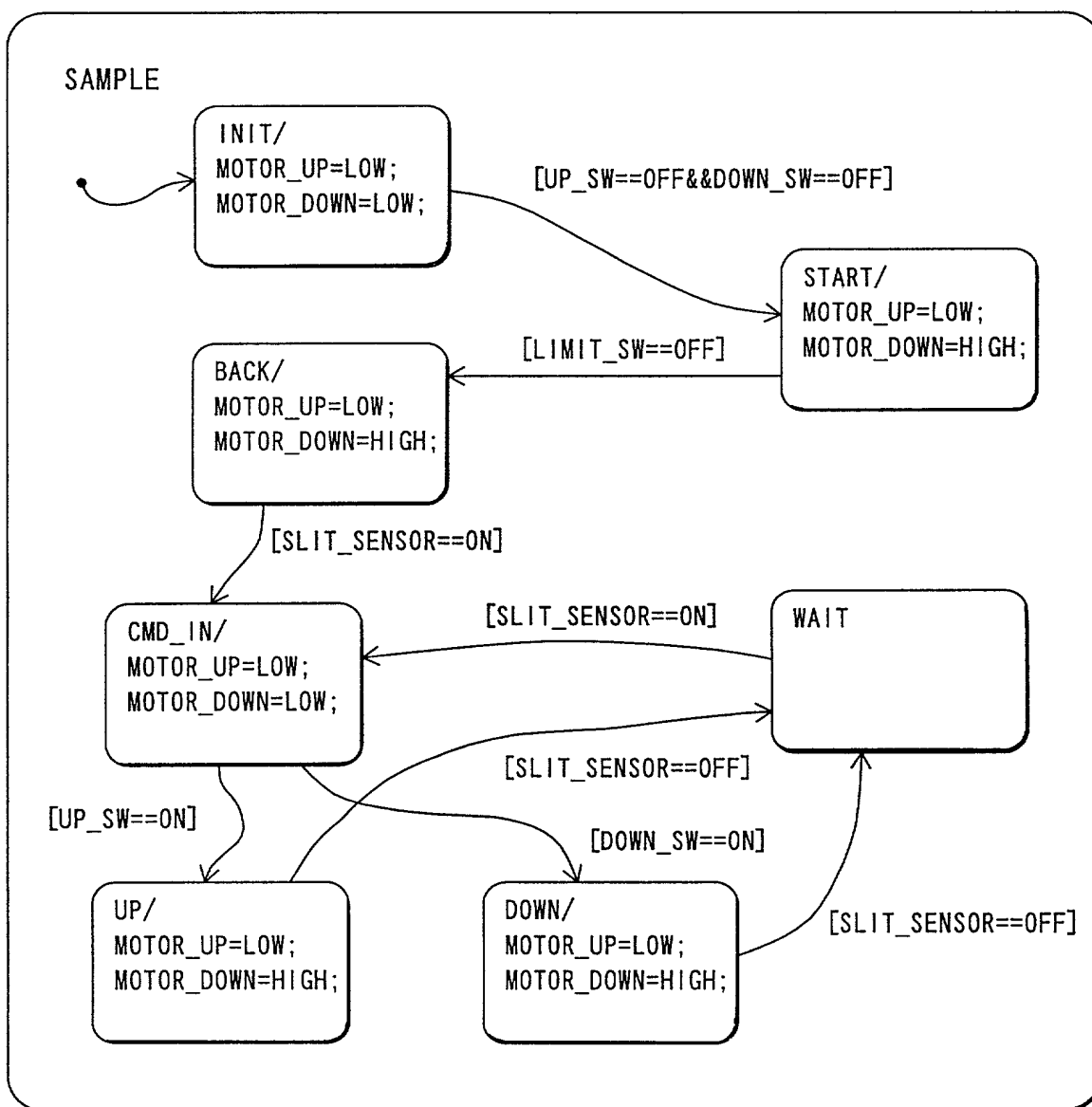


FIG. 10A TASK 1

T_s TIME

FIG. 10B TASK 2

PROCESS FOR T_s TIME

FIG. 10C VIRTUAL MECHANISM

FIG. 11A

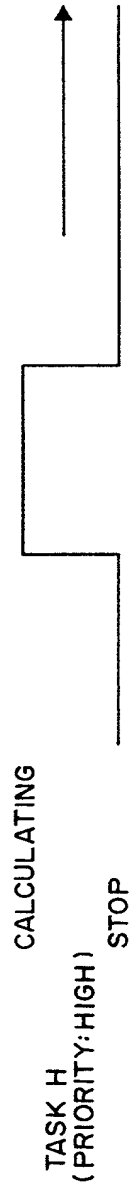


FIG. 11B

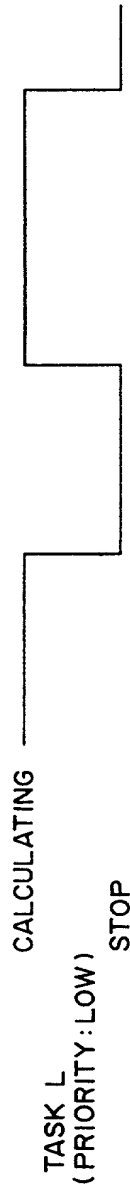


FIG. 12A

TASK 1

FIG. 12B

TASK 2

FIG. 12C

SYNCHRONOUS
TASK

FIG. 12D

VIRTUAL
MECHANISM

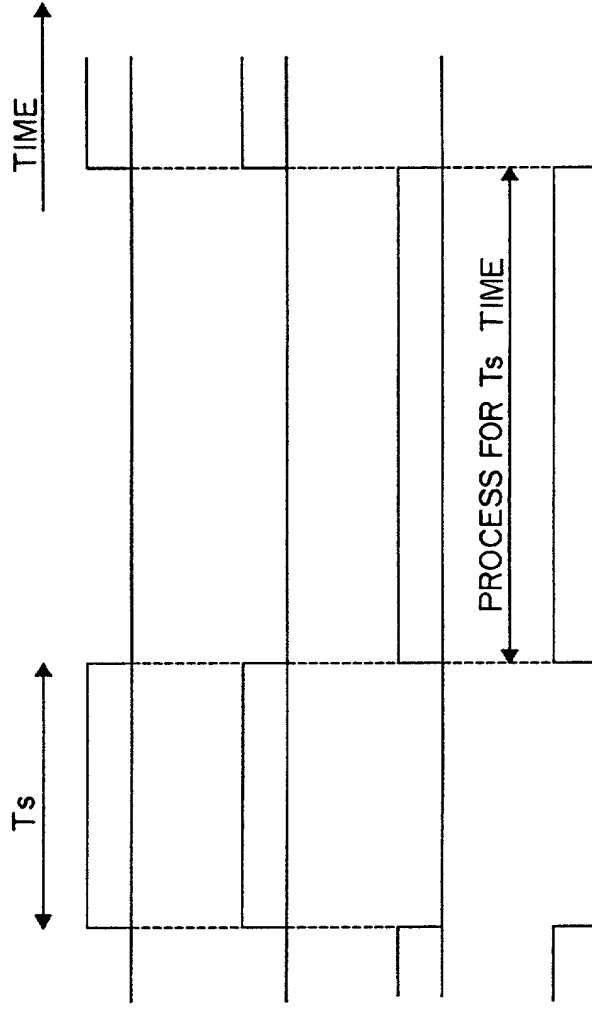


FIG. 14

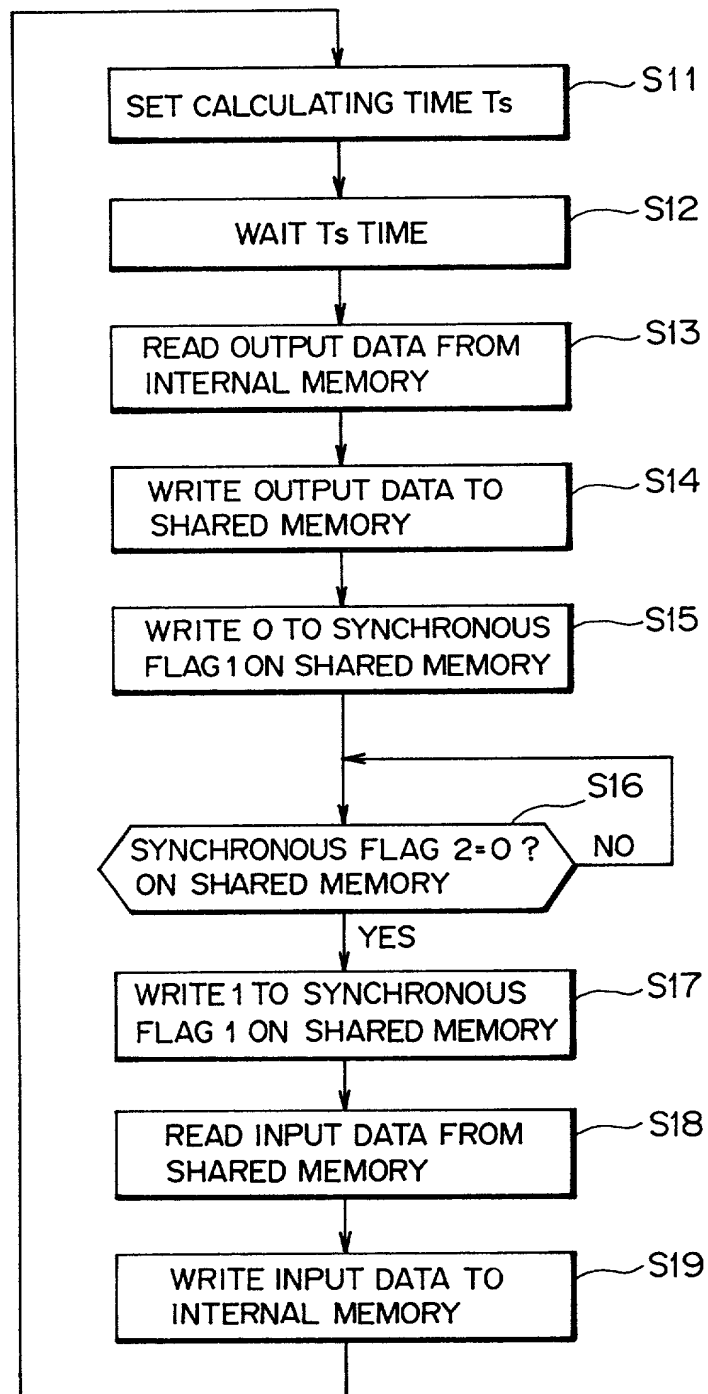


FIG. 15

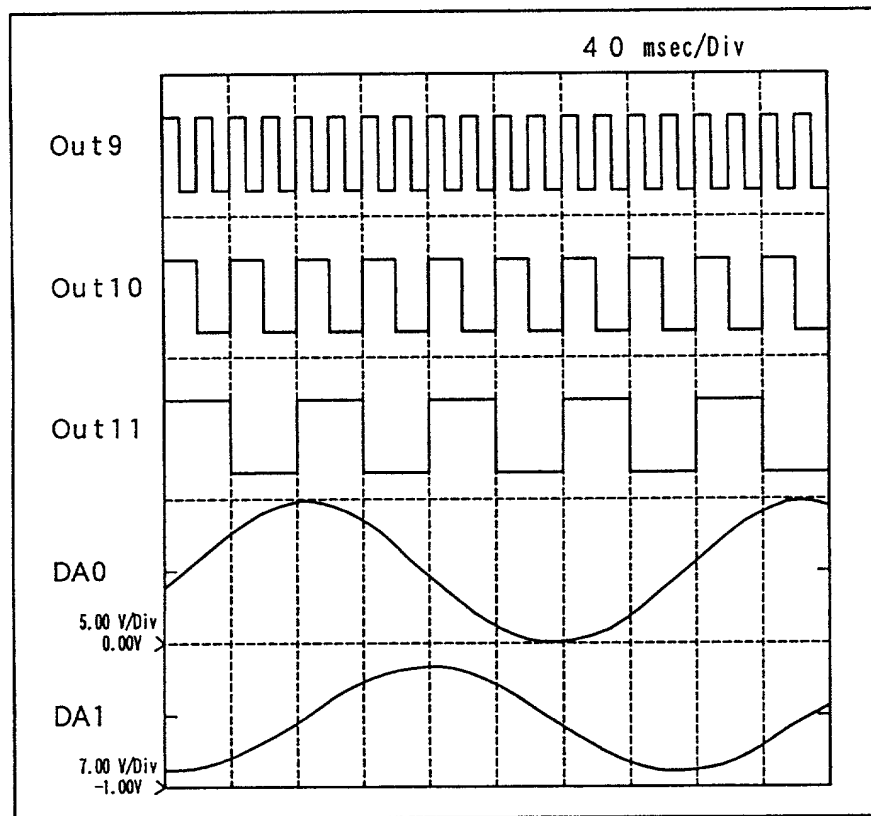


FIG. 16

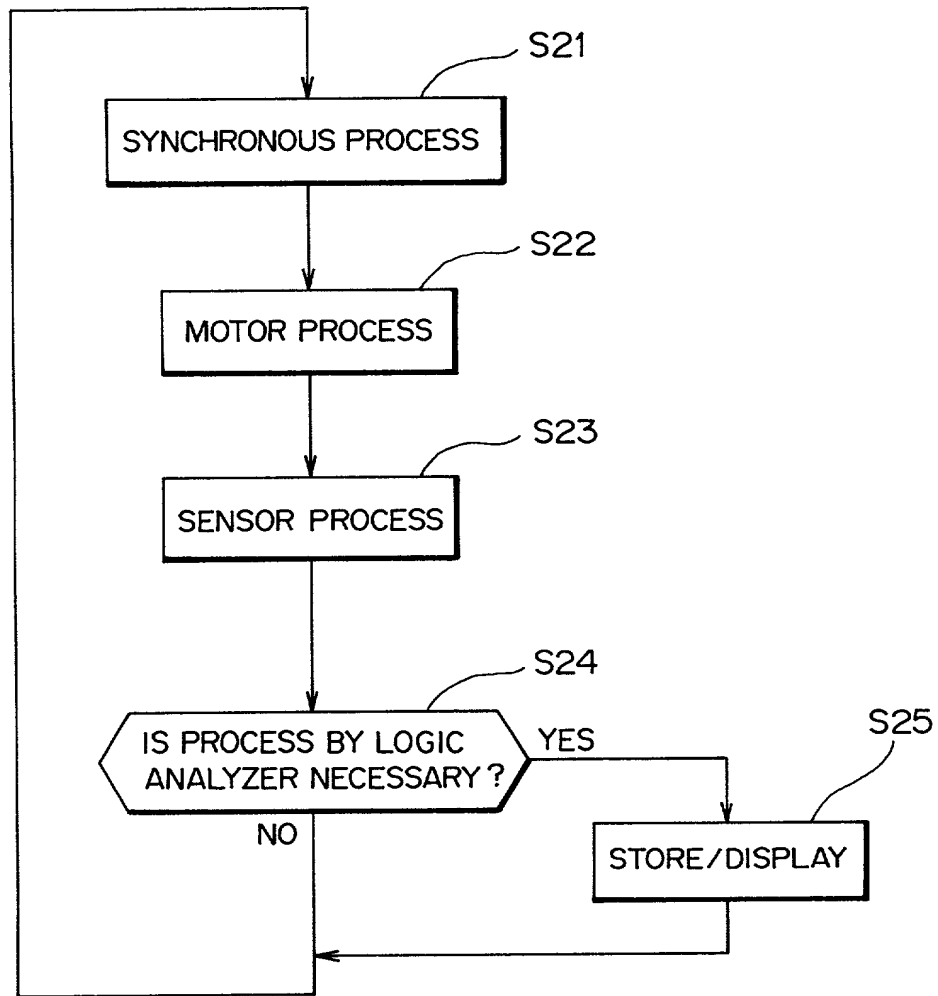


FIG. 17

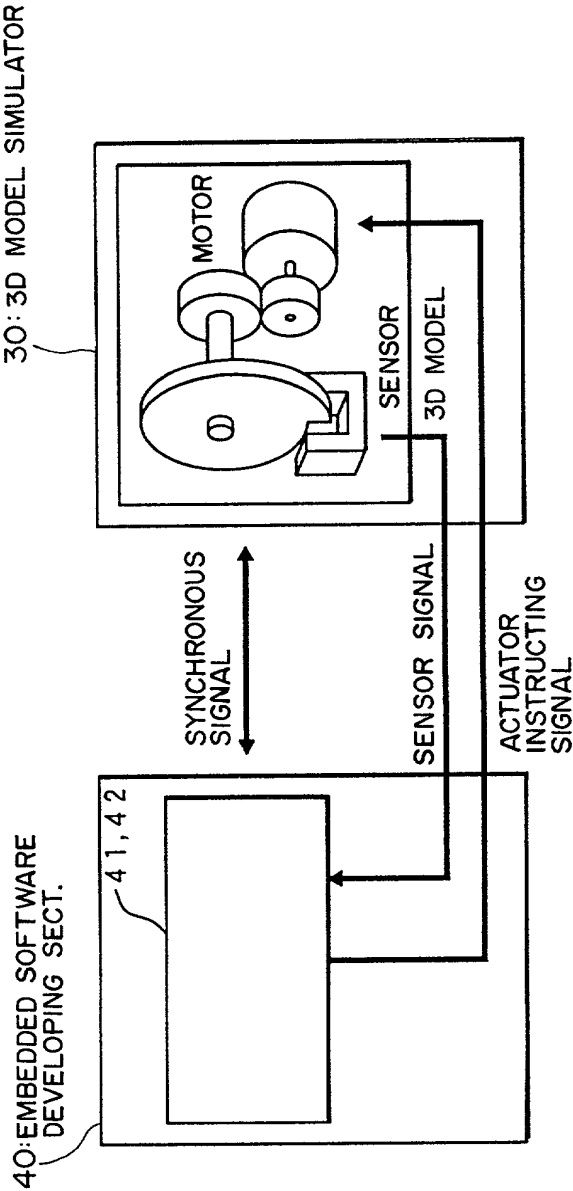


FIG. 18

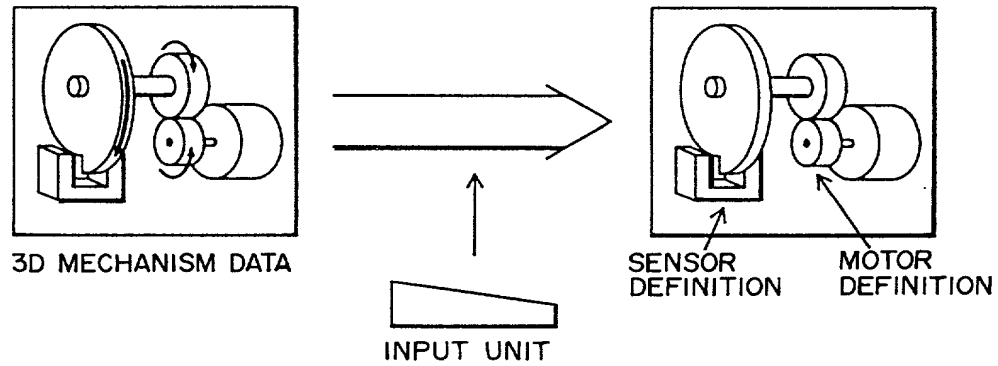


FIG. 19

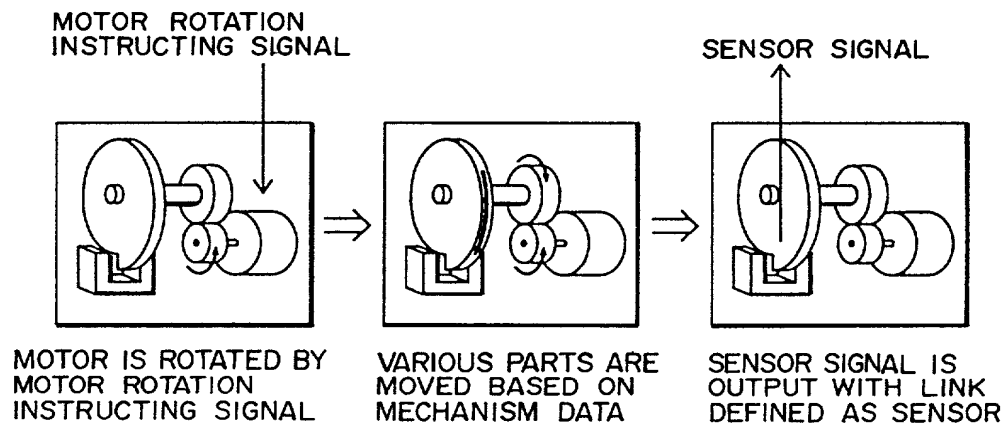


FIG. 20

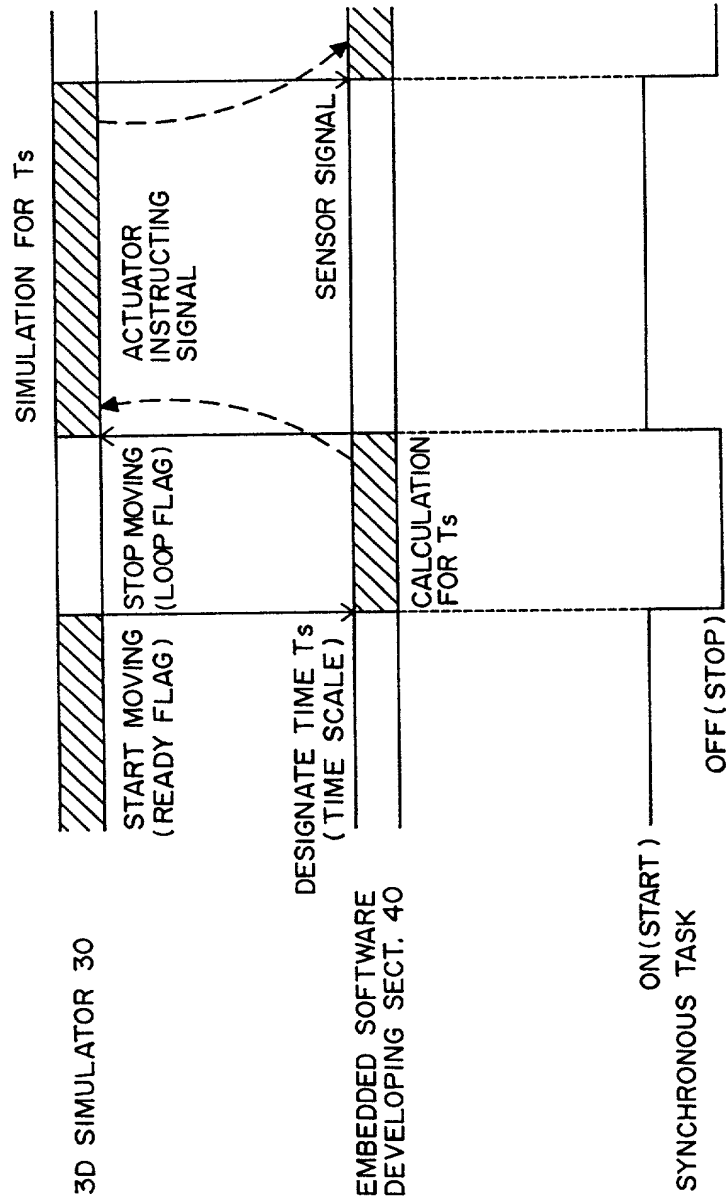


FIG. 21

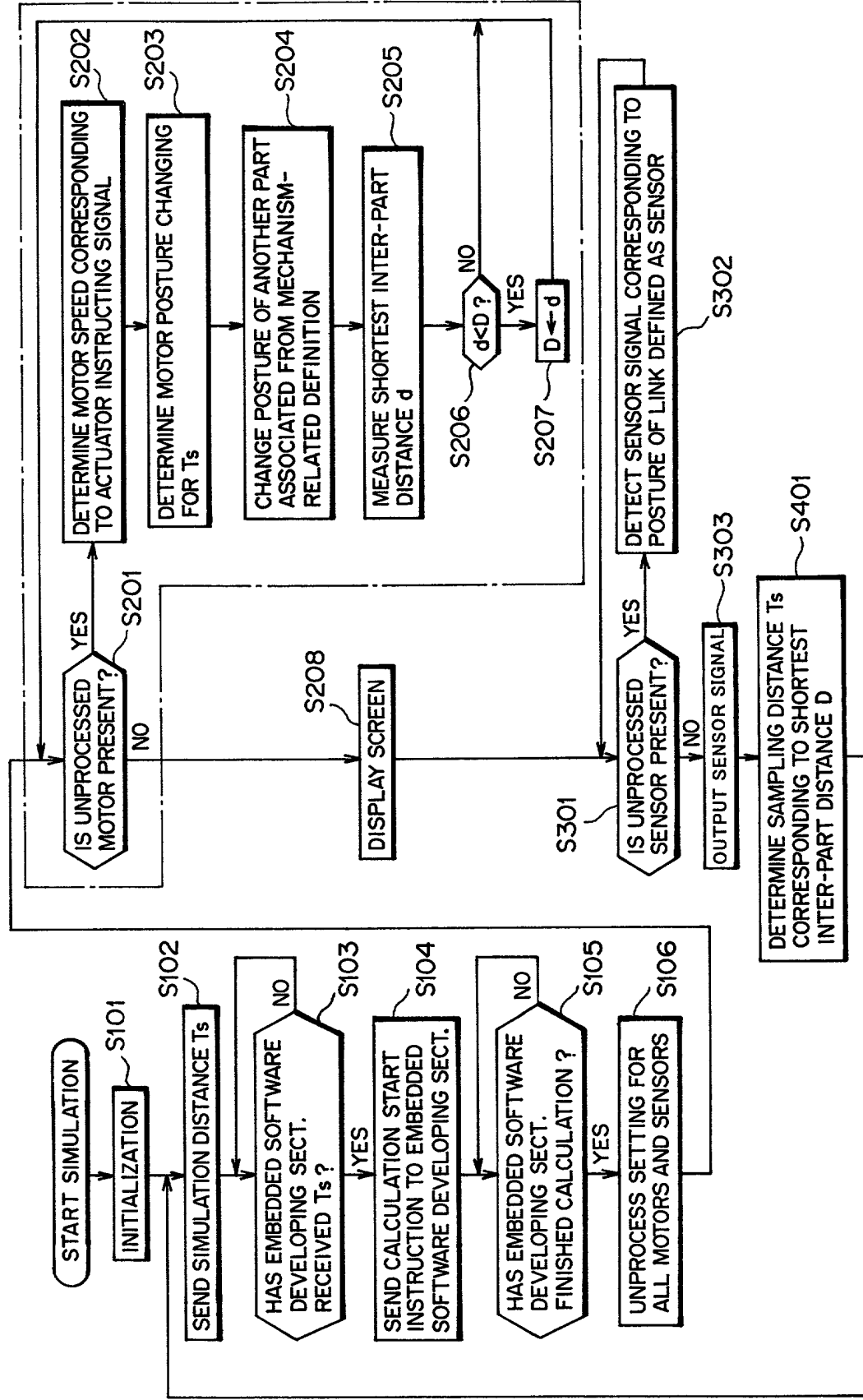


FIG.22

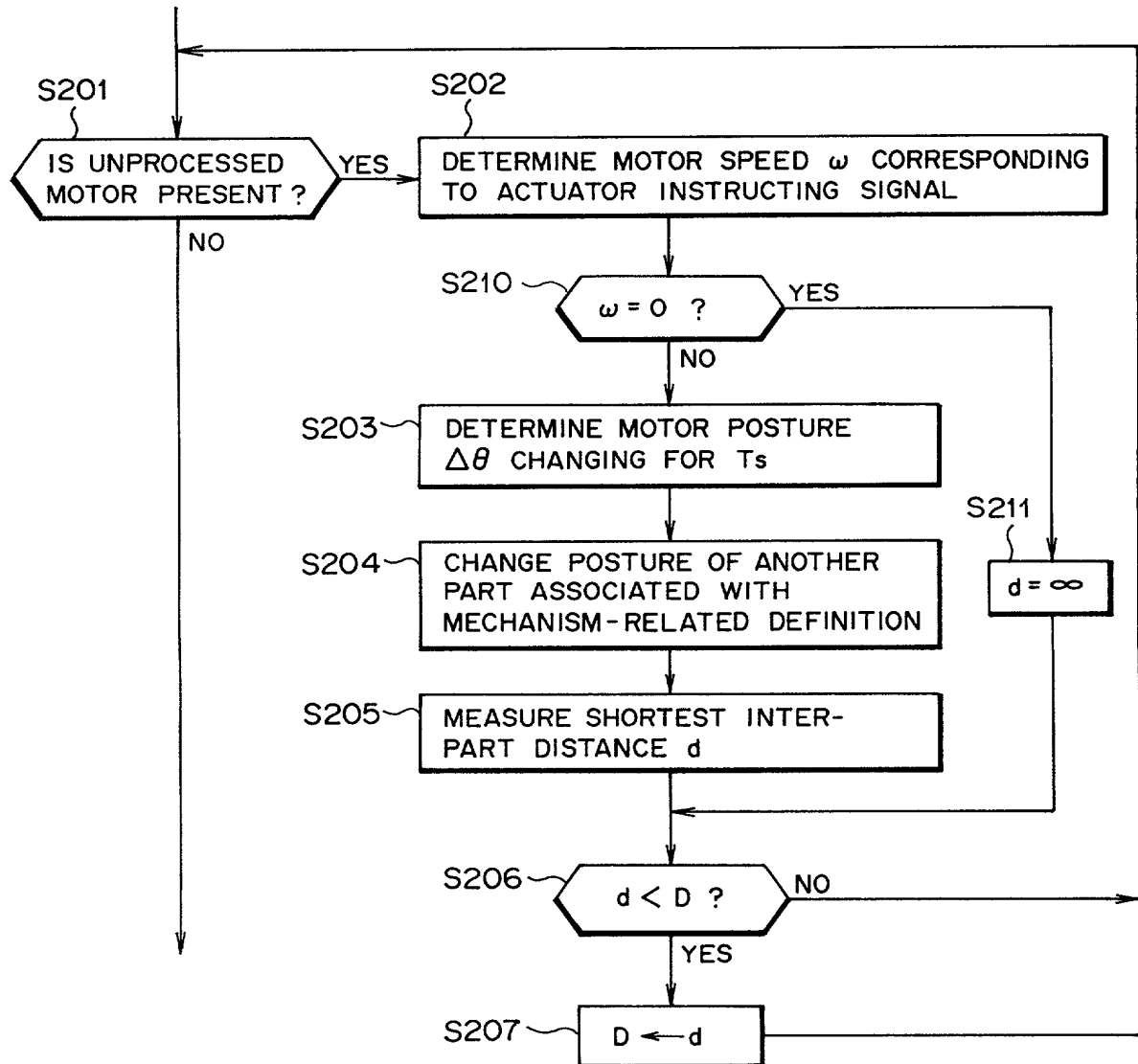


FIG.23

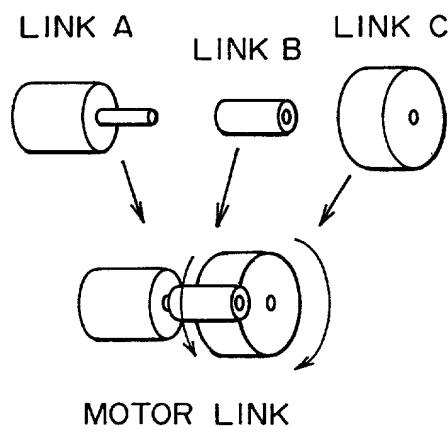


FIG. 24A

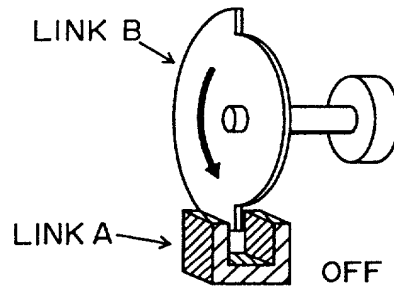


FIG. 24B

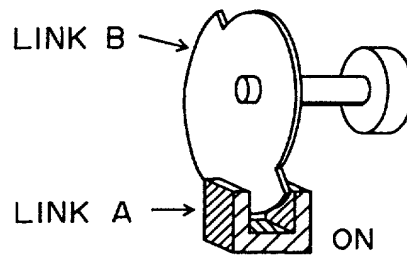


FIG. 25A

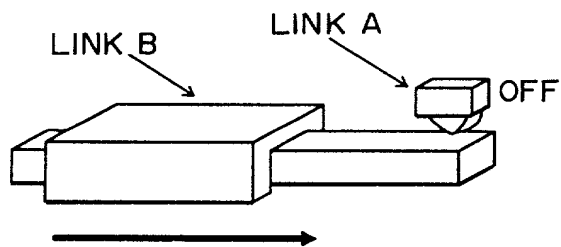


FIG. 25B

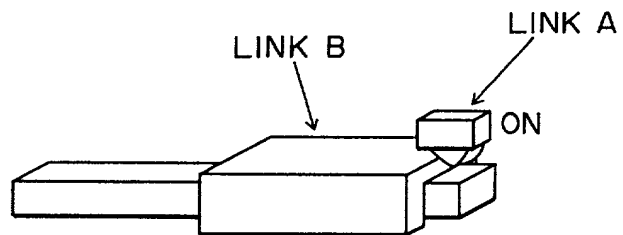


FIG. 26

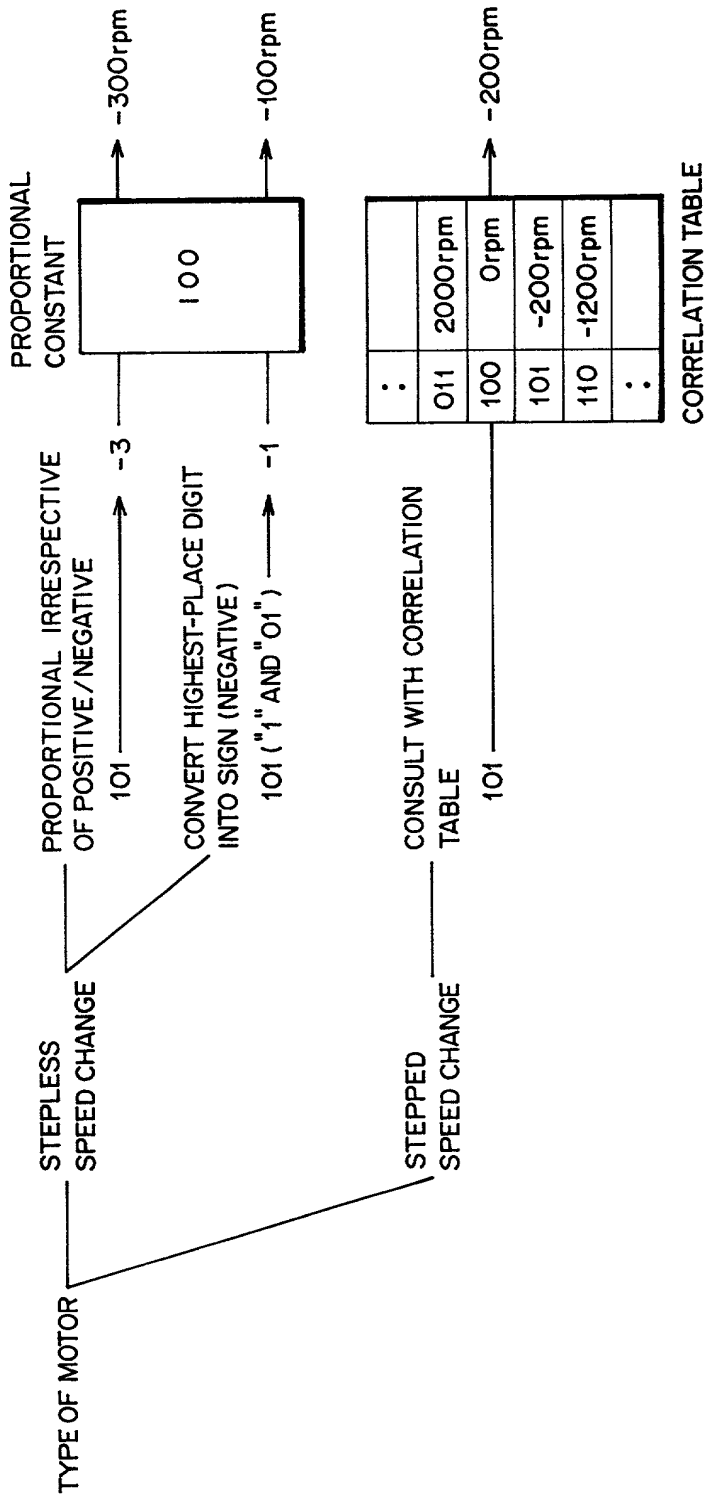


FIG. 27

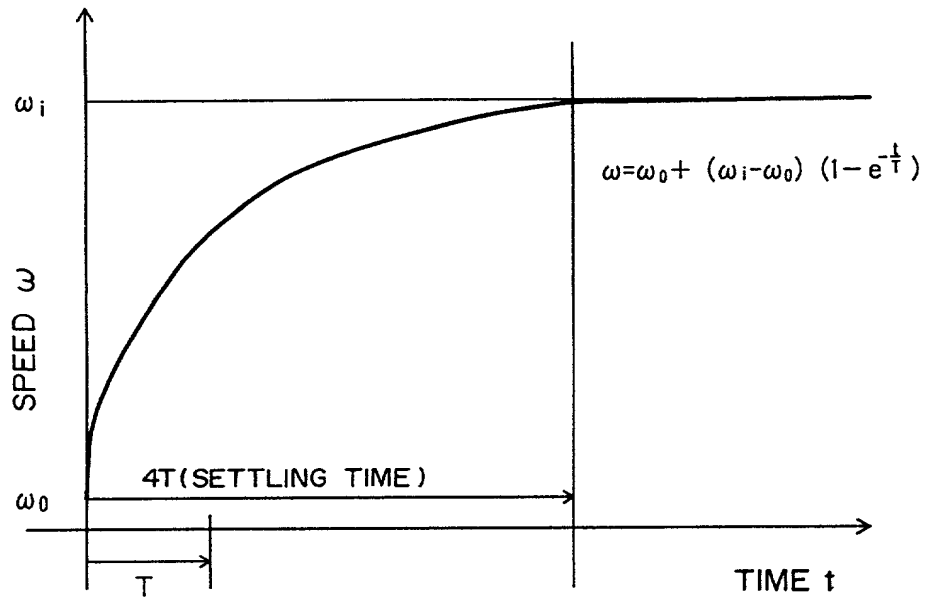


FIG. 28

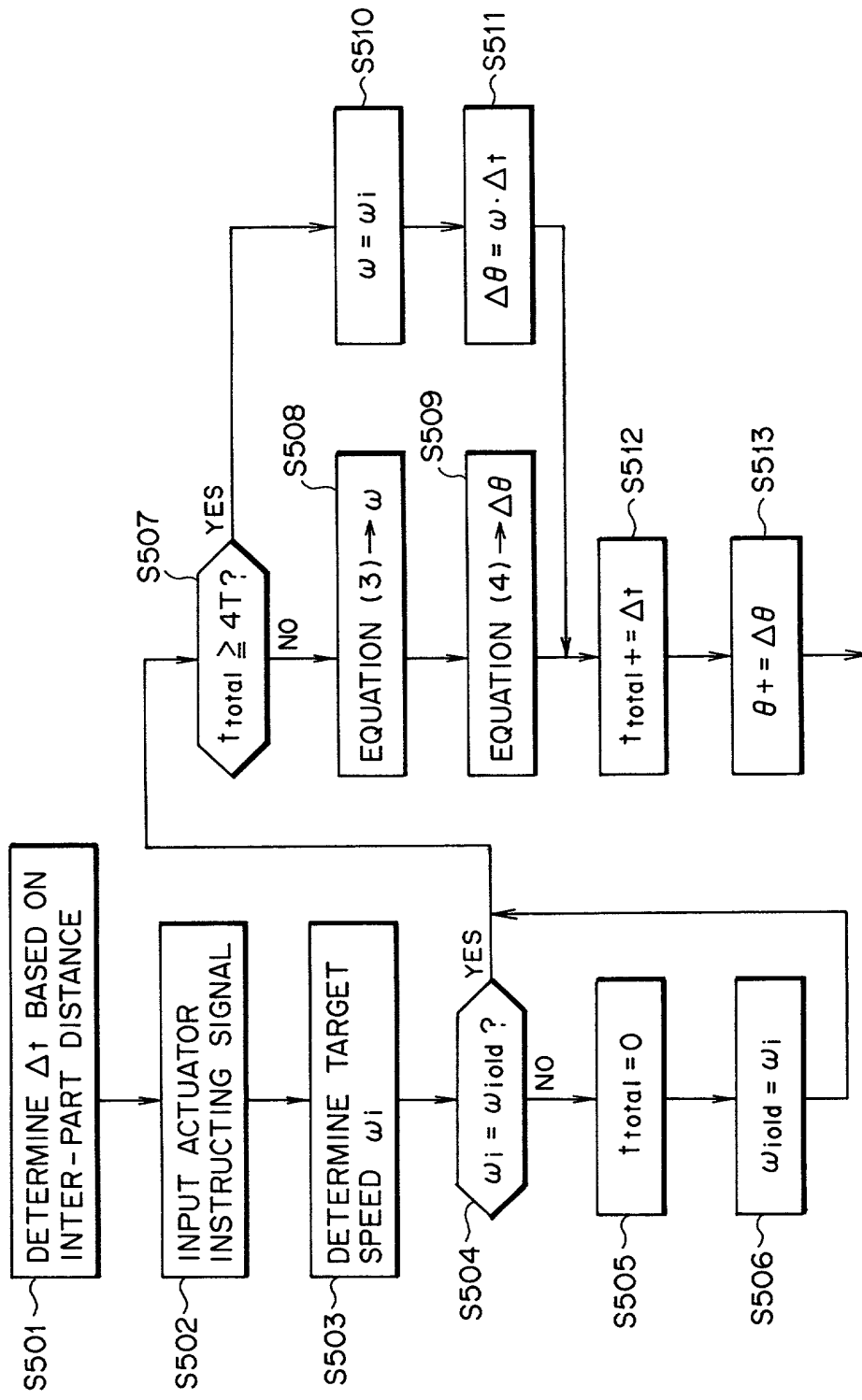


FIG. 29

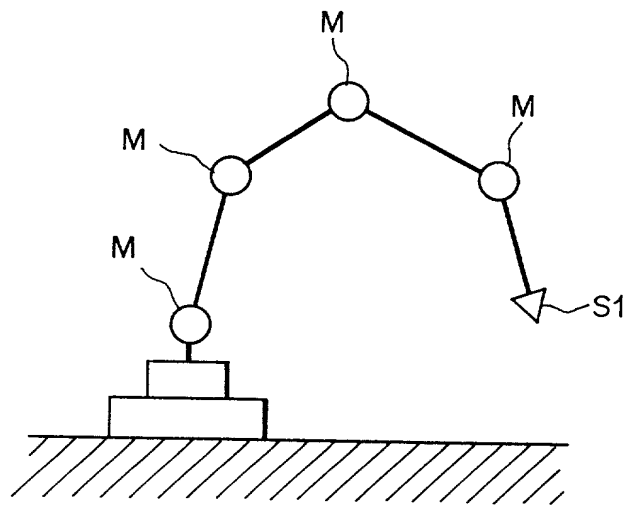


FIG. 30

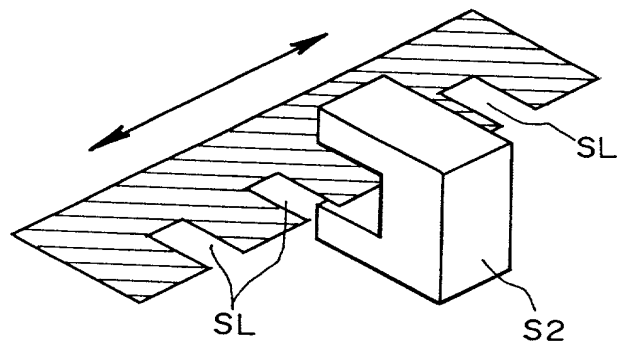


FIG. 31A

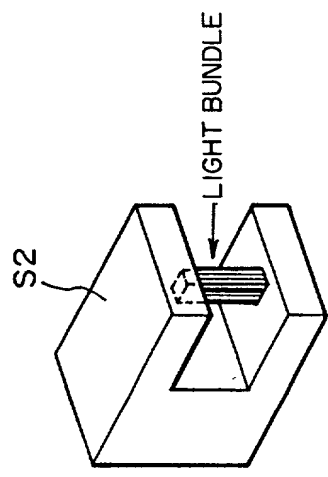


FIG. 31B

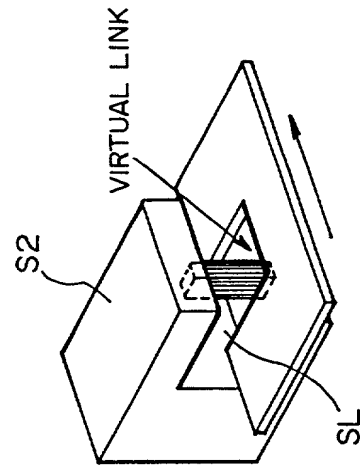


FIG. 31C

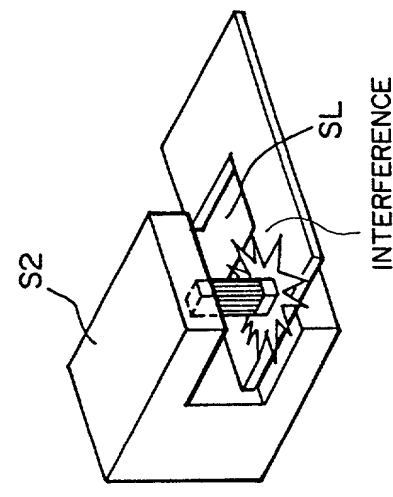


FIG. 32

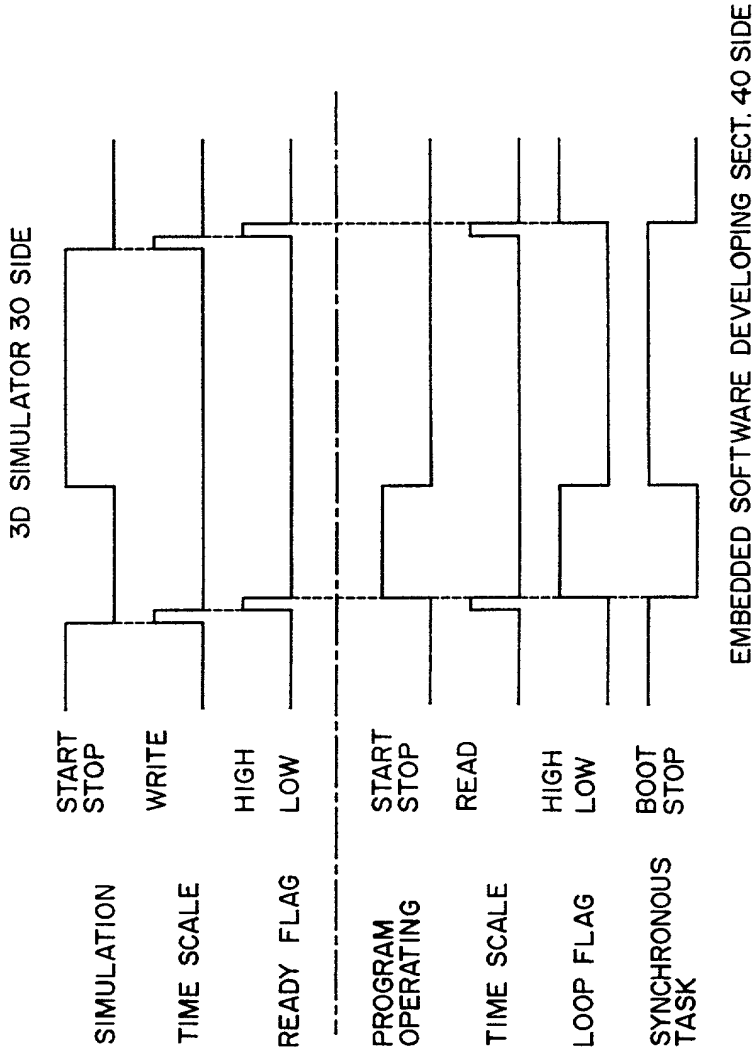


FIG. 33

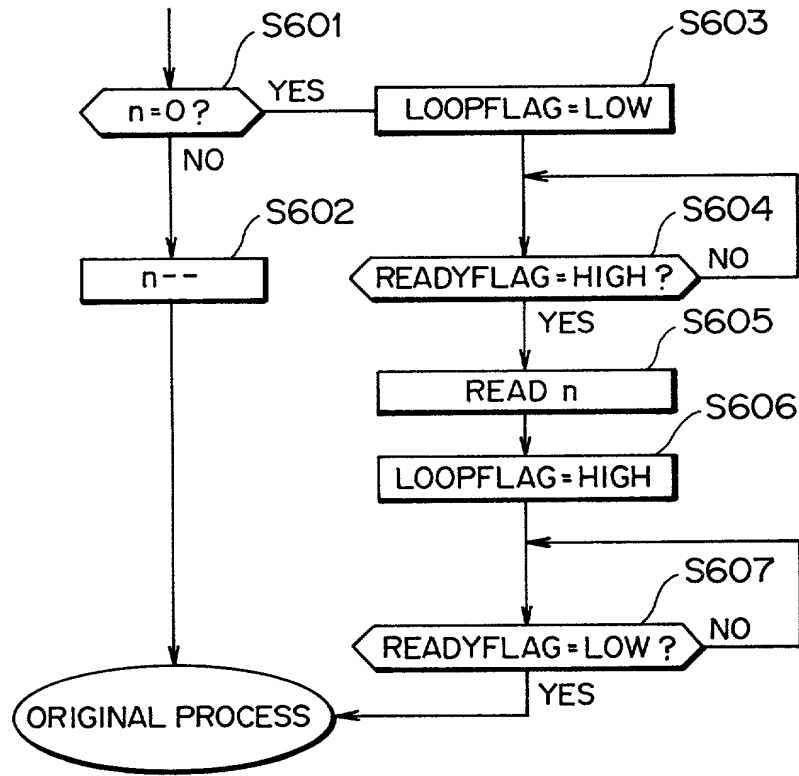


FIG. 34

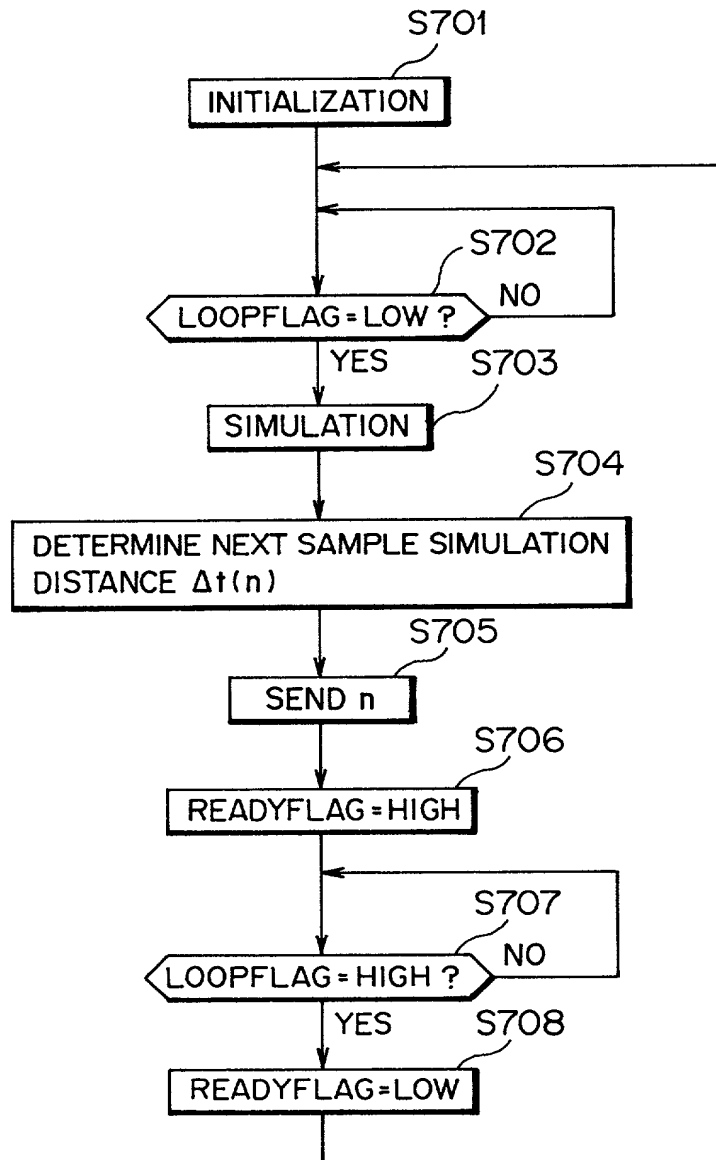


FIG. 35A

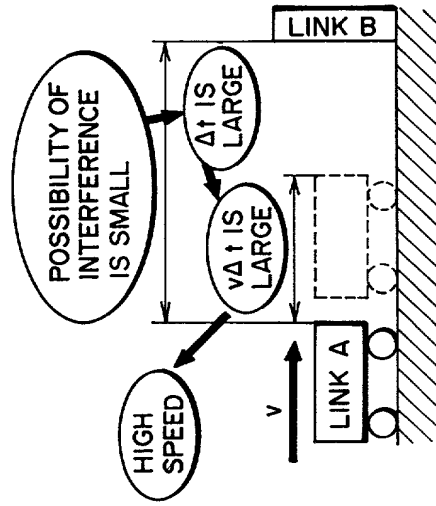


FIG. 35B

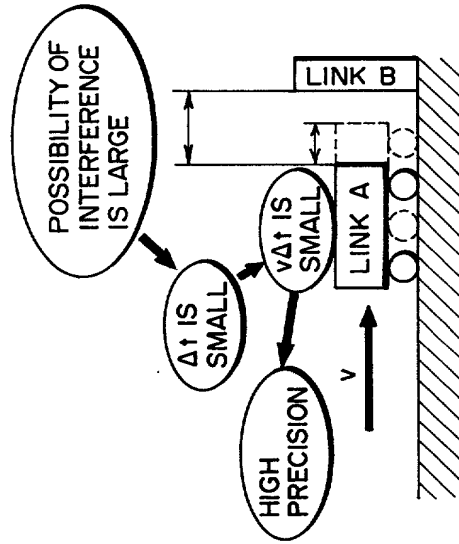


FIG. 36A

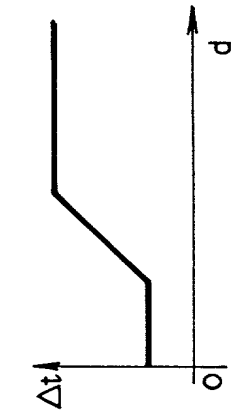


FIG. 36B

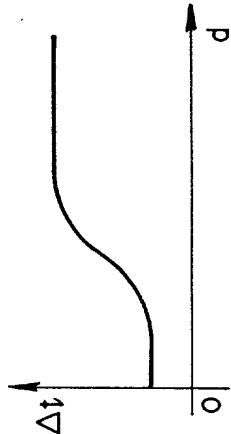


FIG. 36C

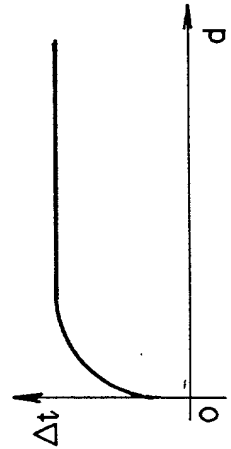


FIG. 37A

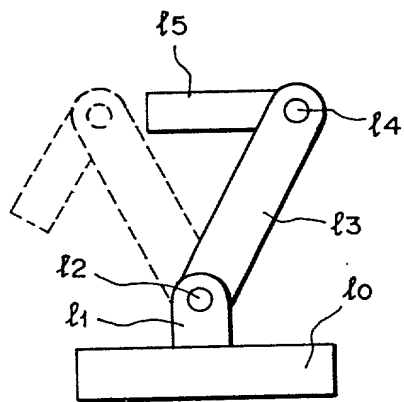


FIG. 37B

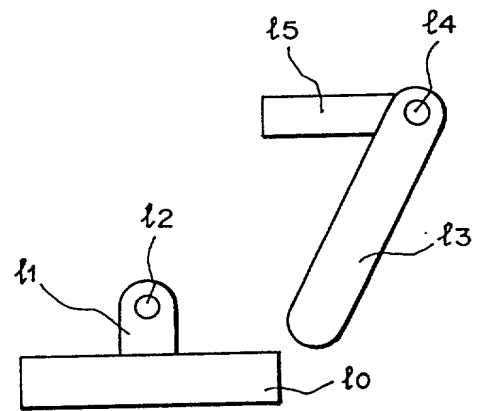


FIG. 38A

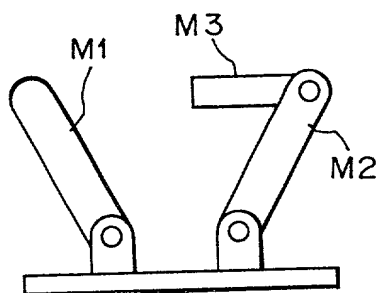


FIG. 38B

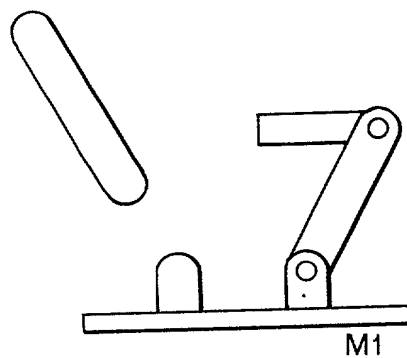


FIG. 38C

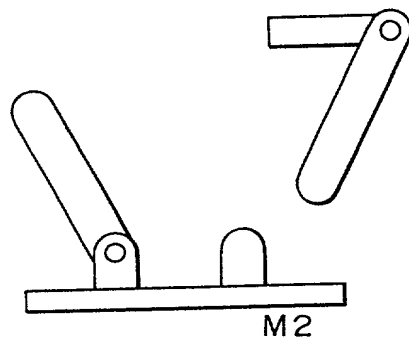


FIG. 38D

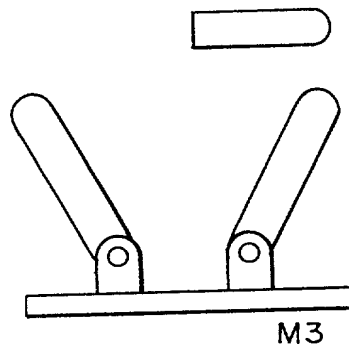


FIG. 39

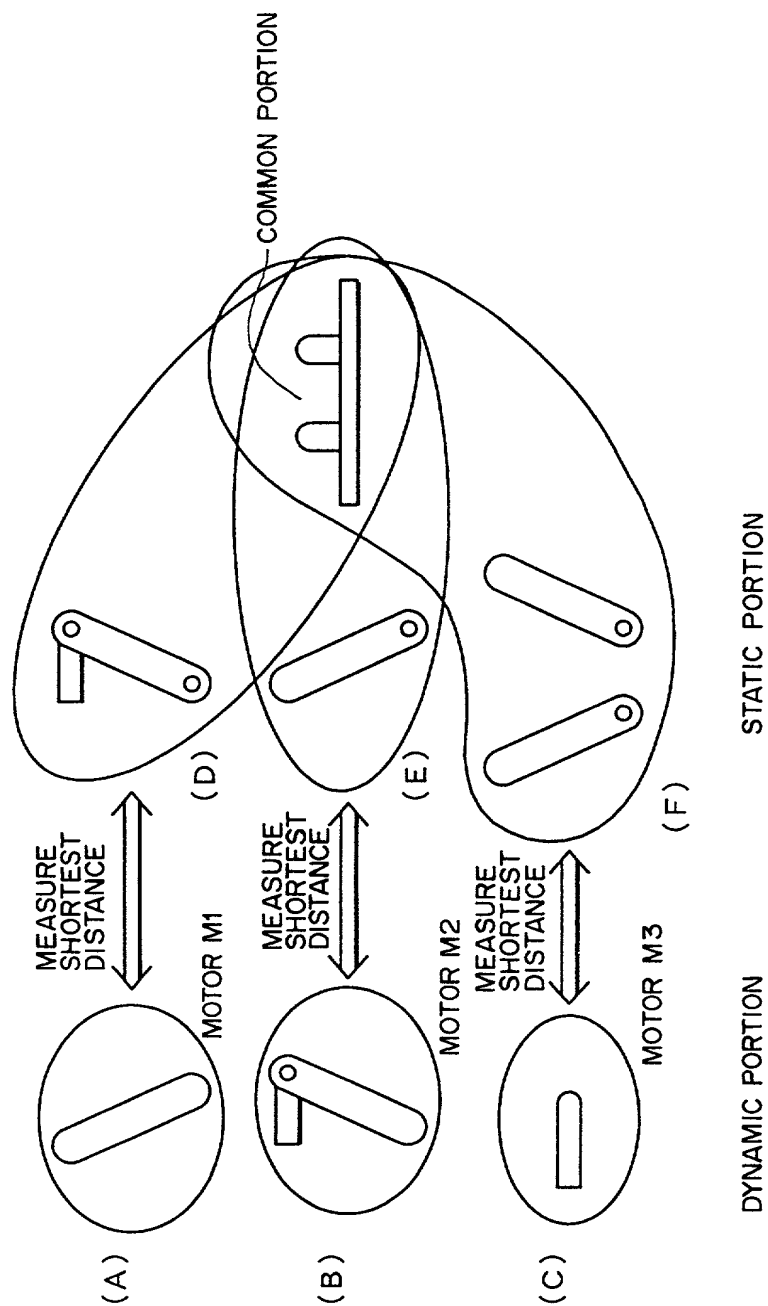


FIG. 40

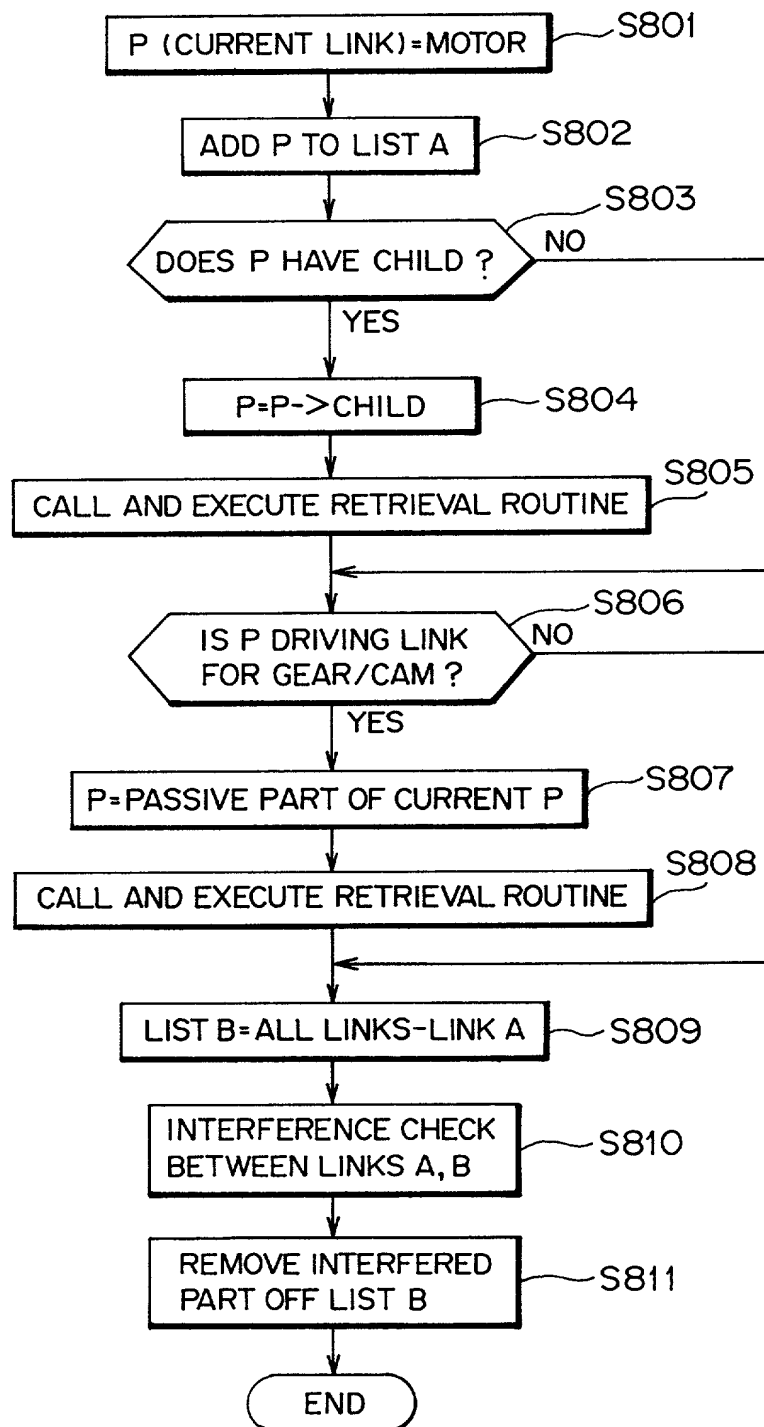


FIG. 41

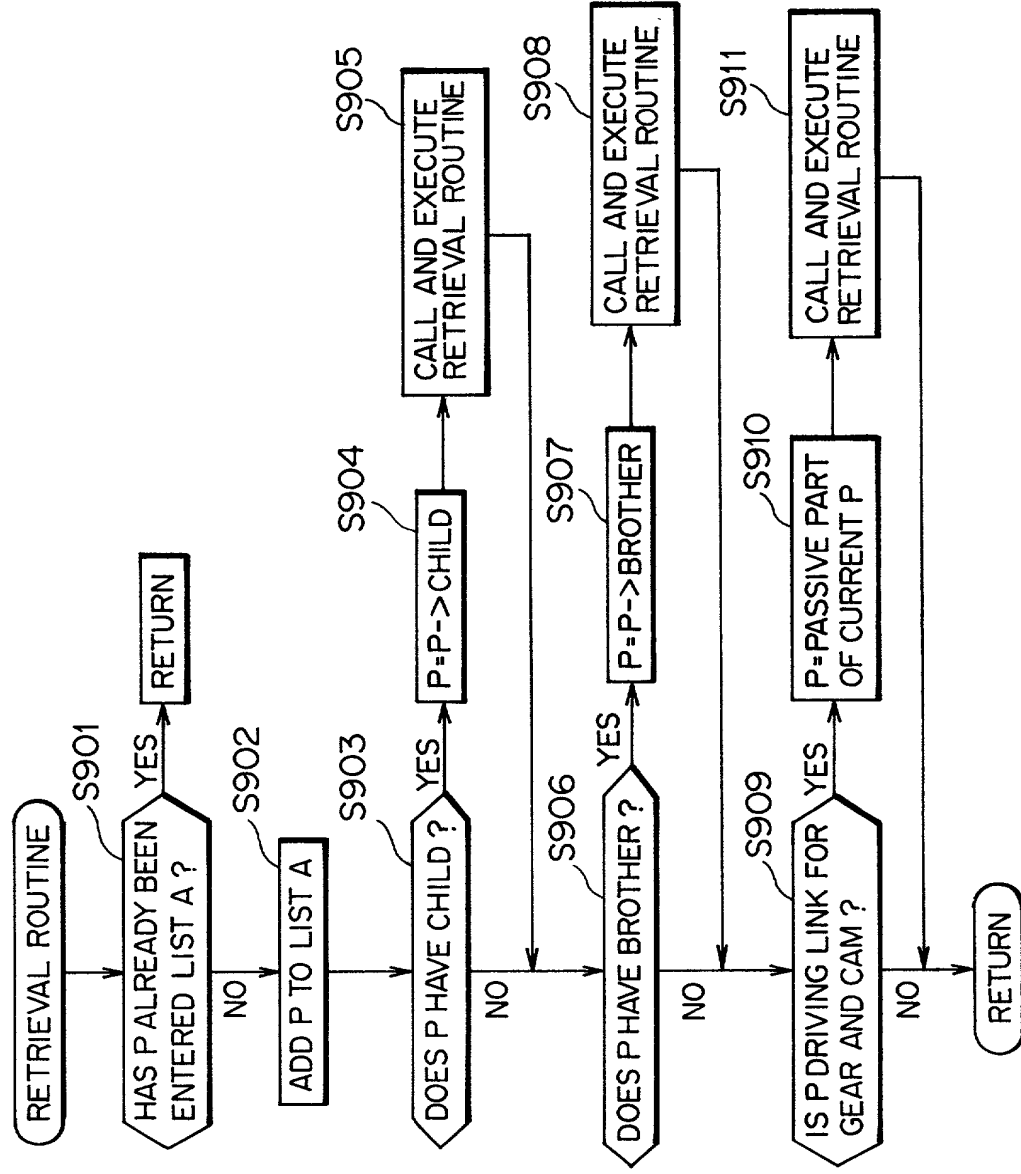


FIG. 42

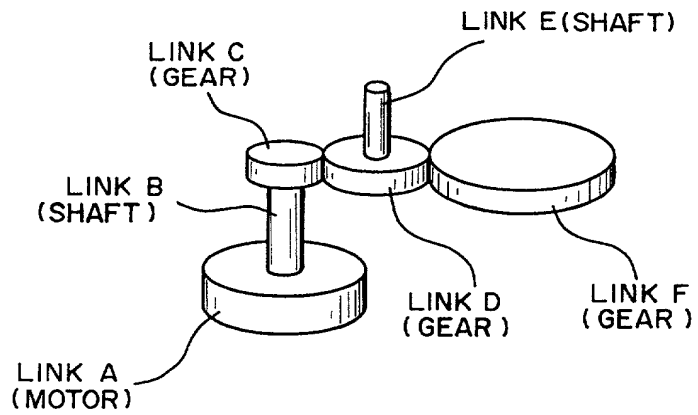


FIG. 43

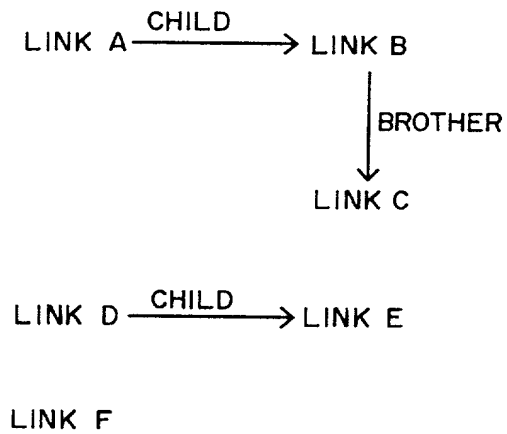


FIG. 44A

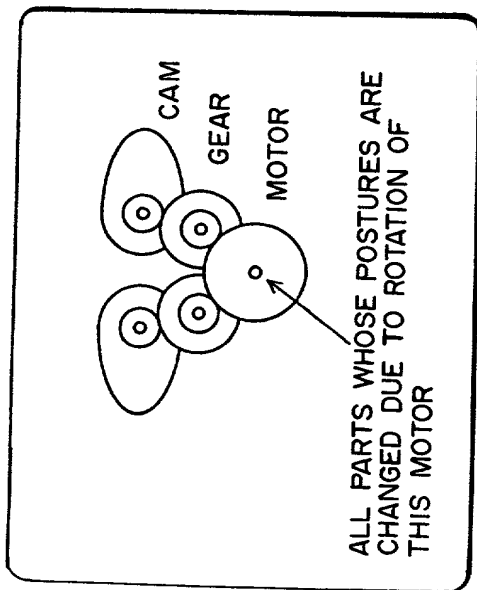


FIG. 44B

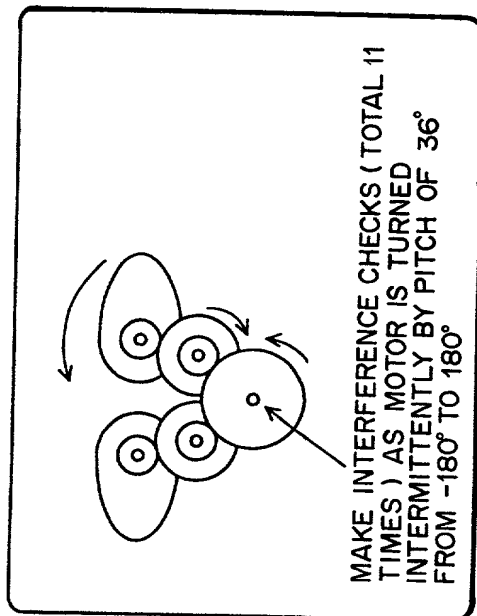


FIG. 44C

LINK A AND LINK B	2
LINK A AND LINK C	3
LINK B AND LINK D	4
LINK B AND LINK E	11
LINK C AND LINK E	8
LINK D AND LINK E	11

SETS OF INTERFERED PARTS, AND FREQUENCY OF INTERFERENCE

FIG. 44D

LINK A AND LINK B	2
LINK A AND LINK C	3
LINK B AND LINK D	4
LINK B AND LINK E	11
LINK C AND LINK E	8
LINK D AND LINK E	11

EXCLUDE, FROM LIST, SETS WHOSE PARTS INTERFERED IN ALL INTERFERENCE CHECKS

FIG. 45

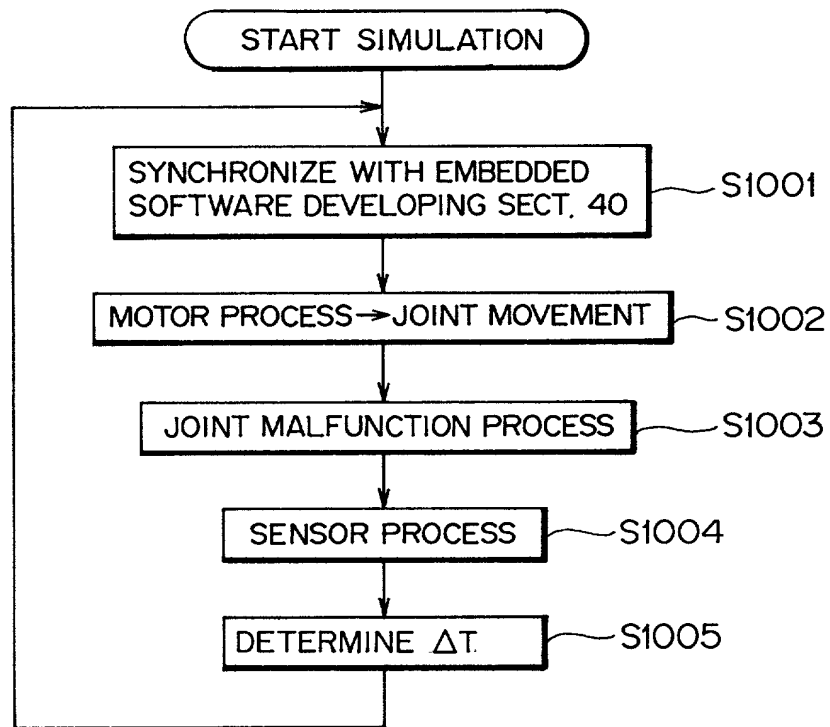


FIG. 46

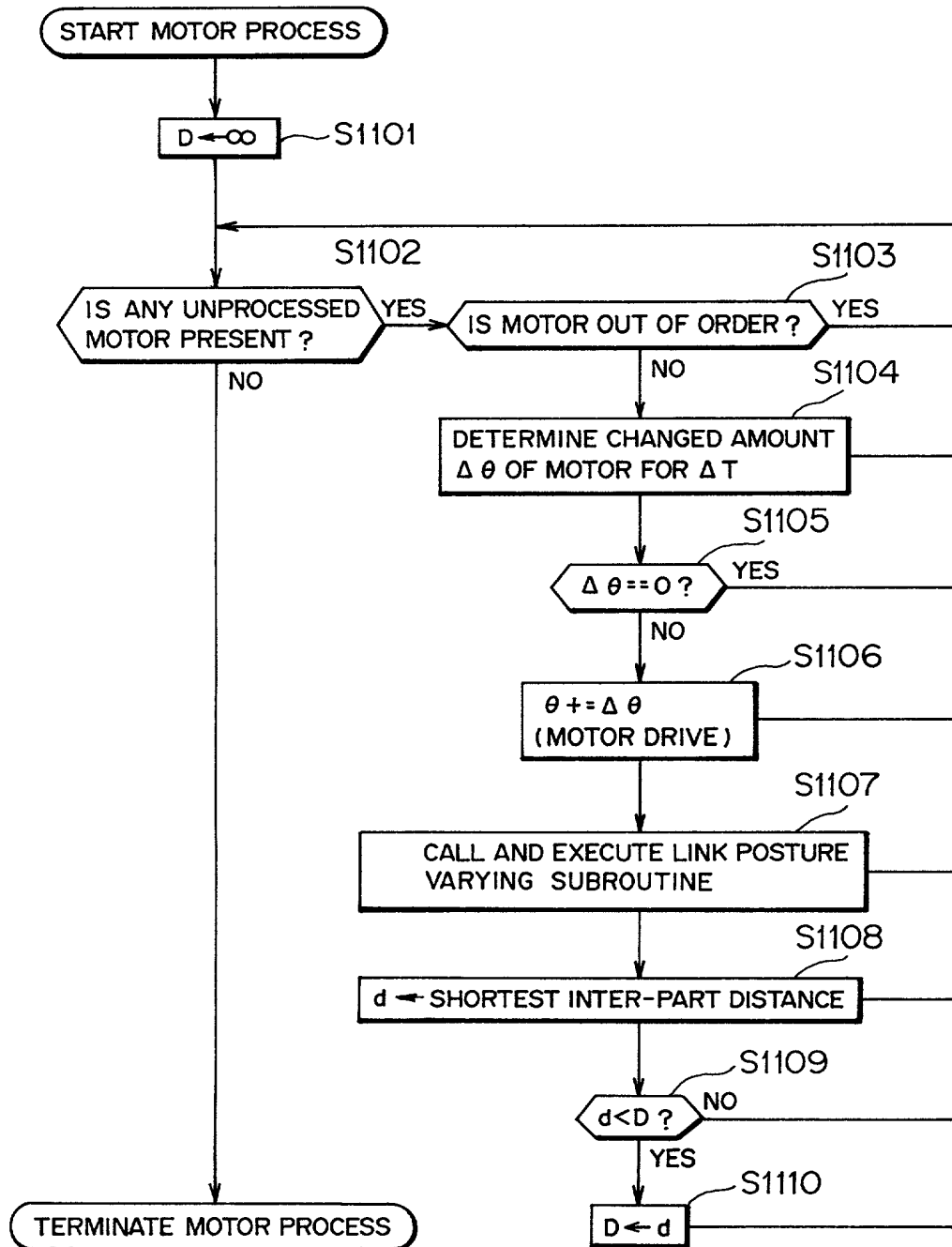


FIG. 47

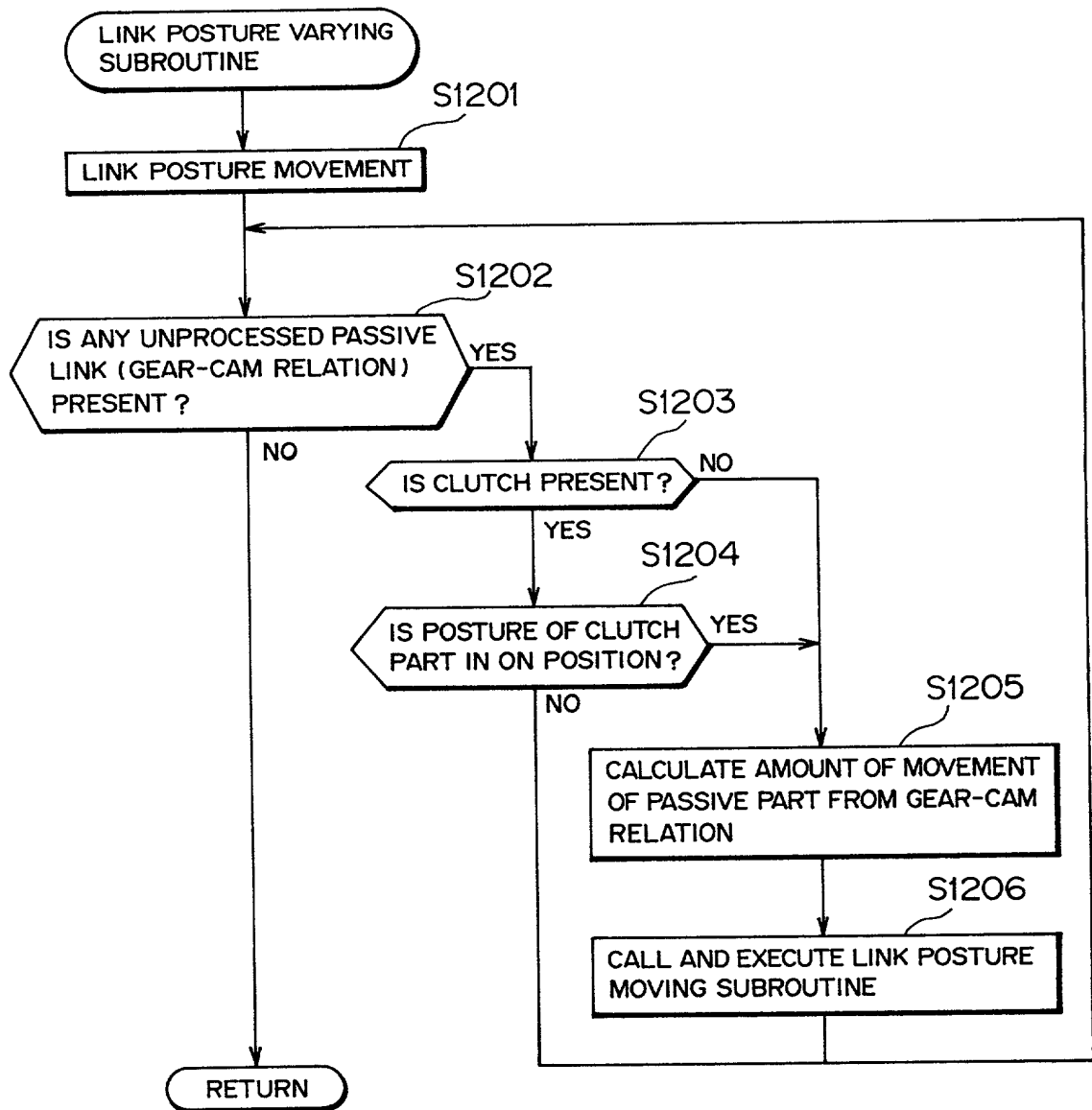


FIG. 48

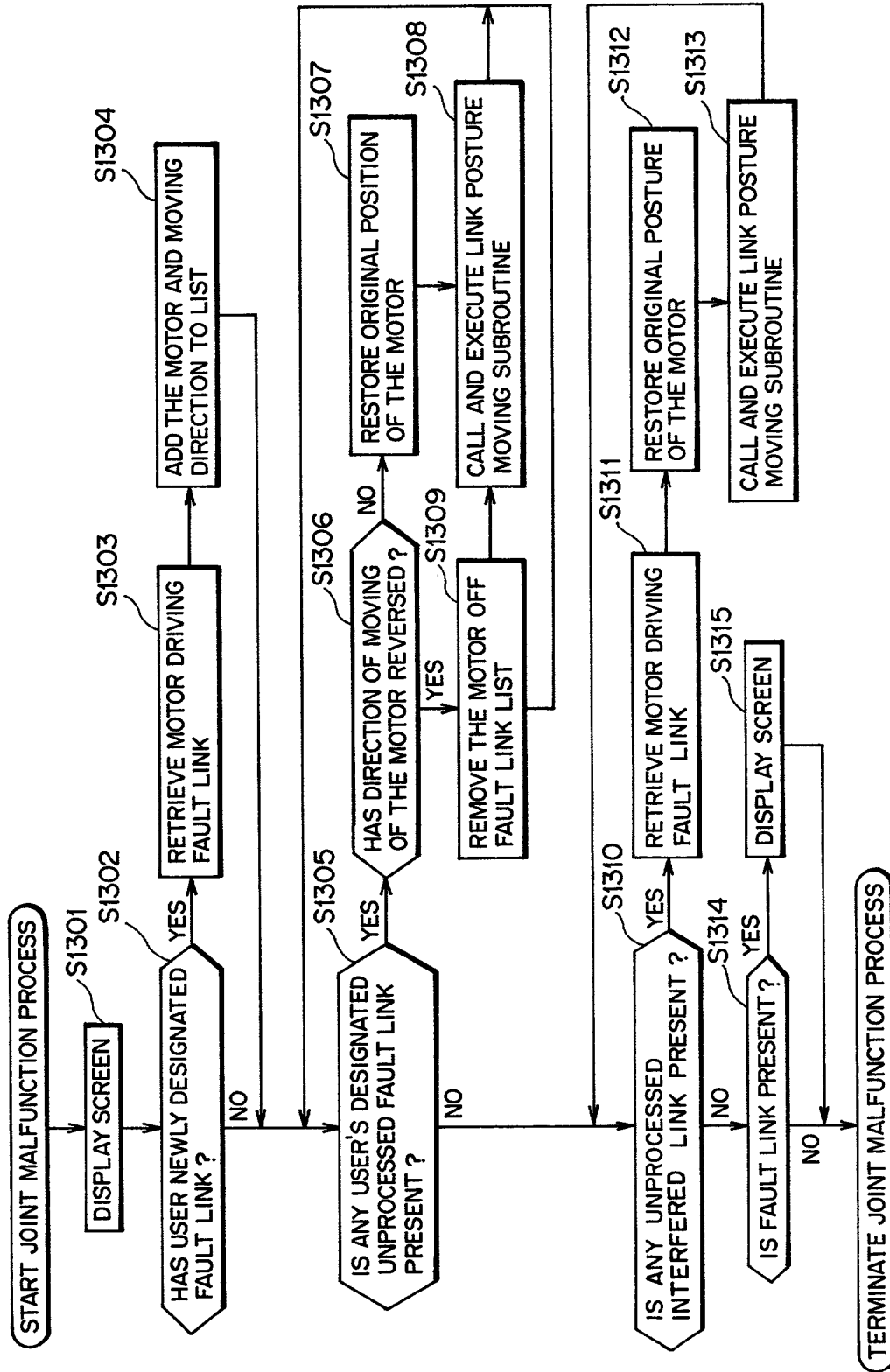


FIG. 49

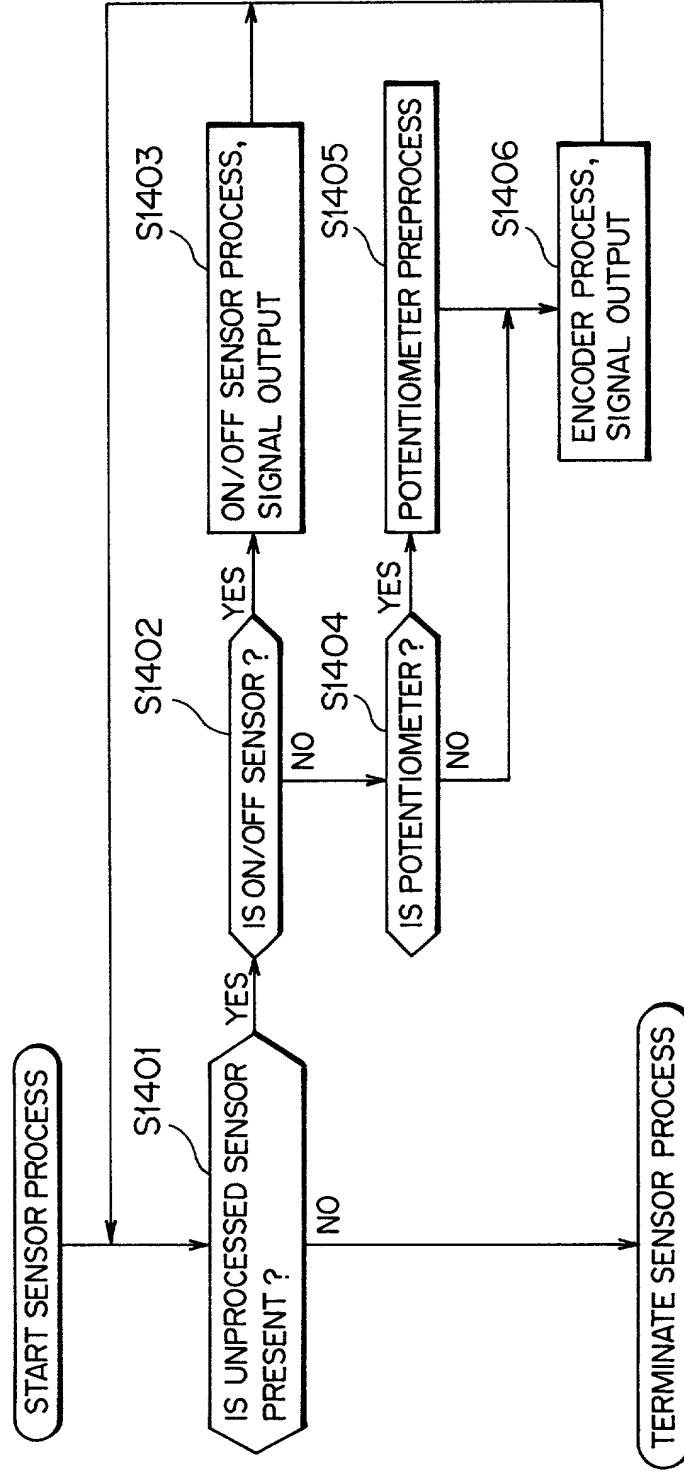


FIG. 50

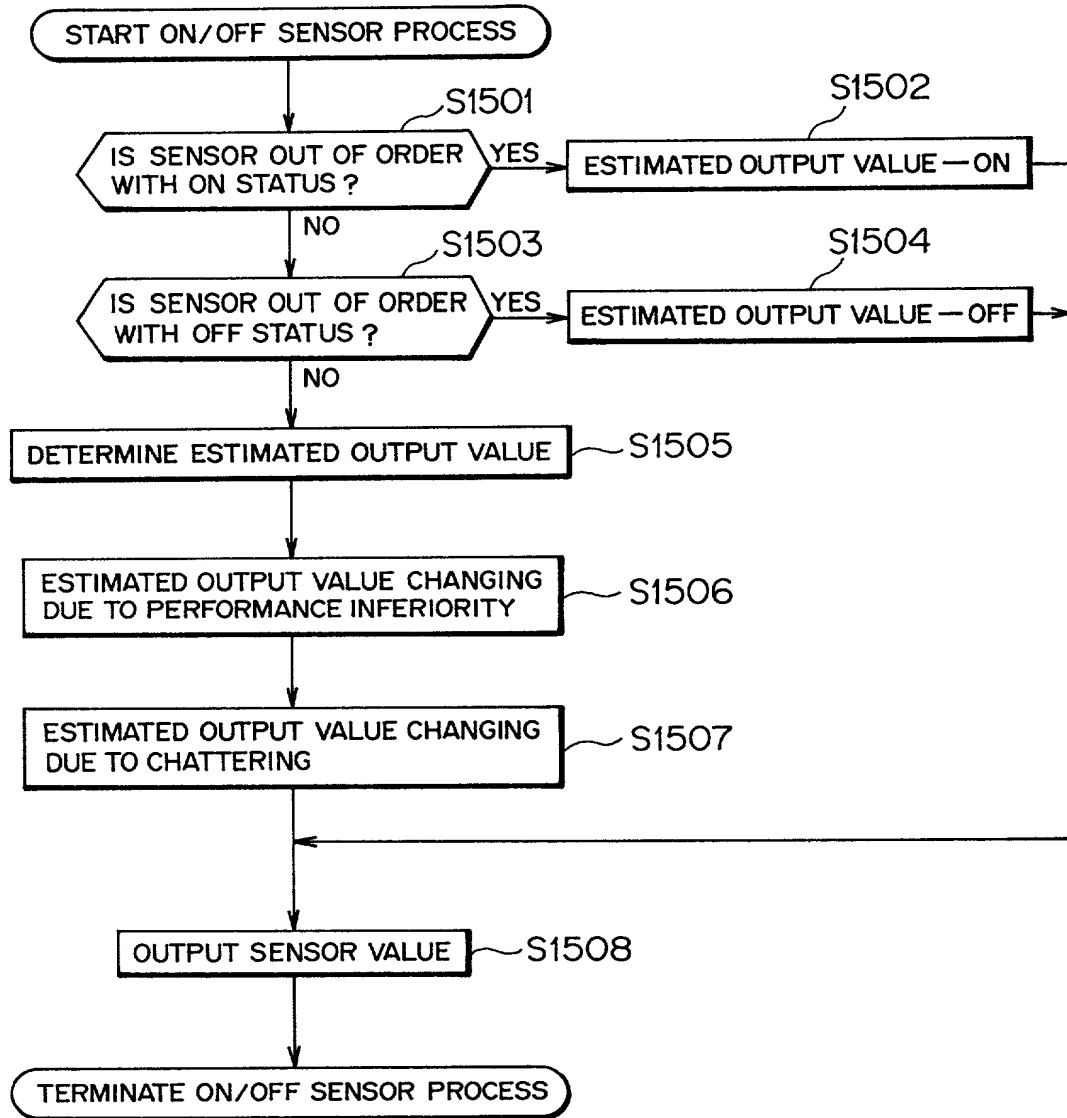


FIG. 51

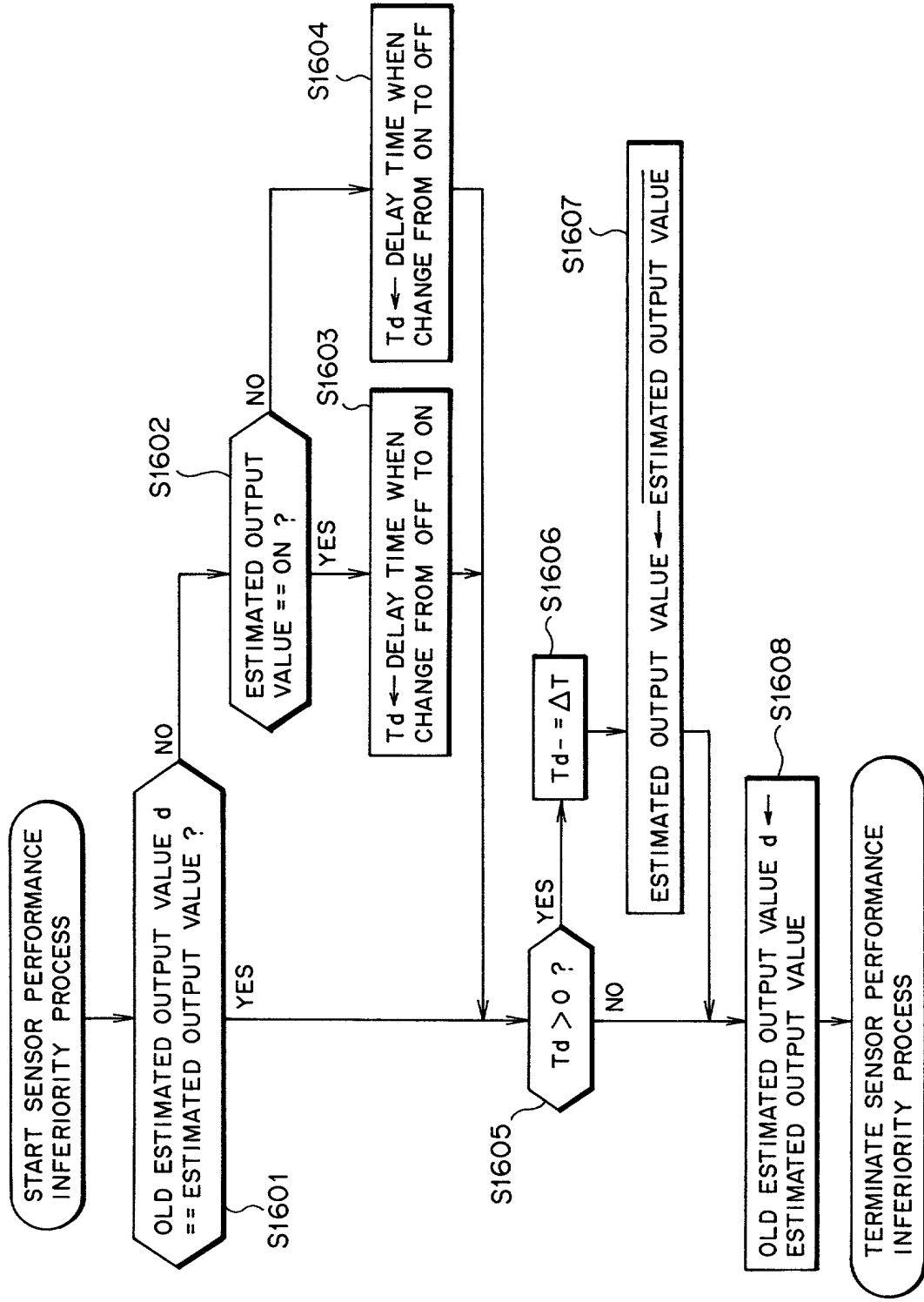


FIG. 52

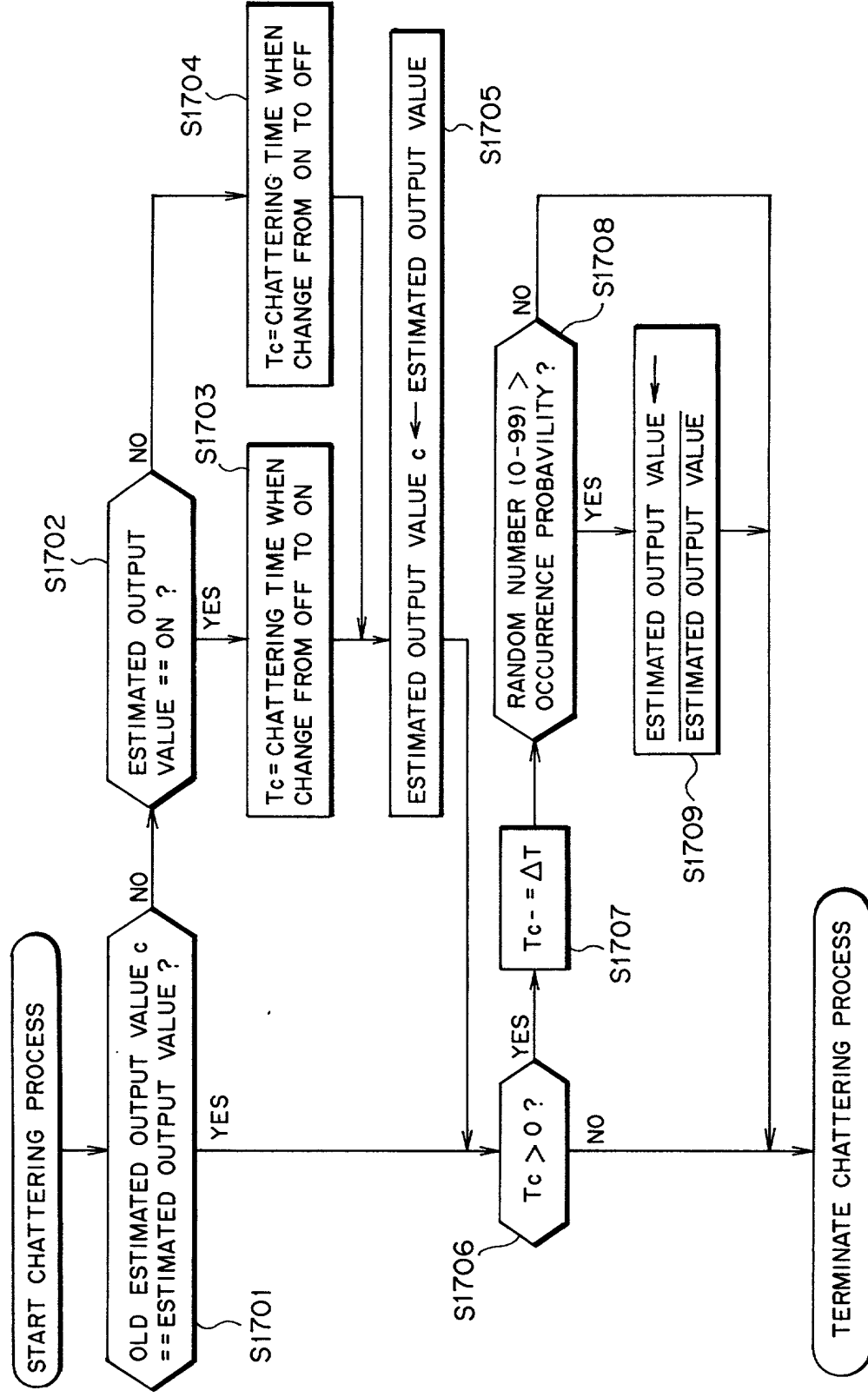


FIG. 53

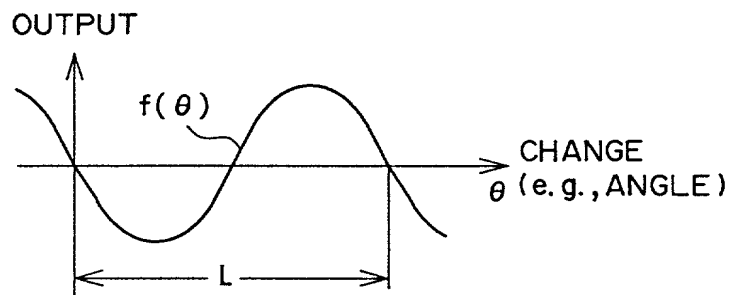


FIG. 54A

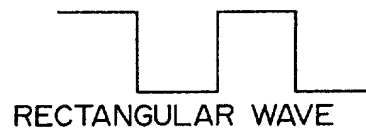


FIG. 54B

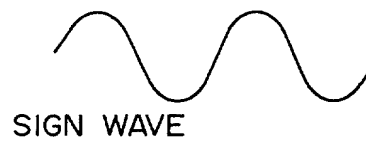


FIG. 54C



FIG. 54D



FIG. 54E



FIG. 55

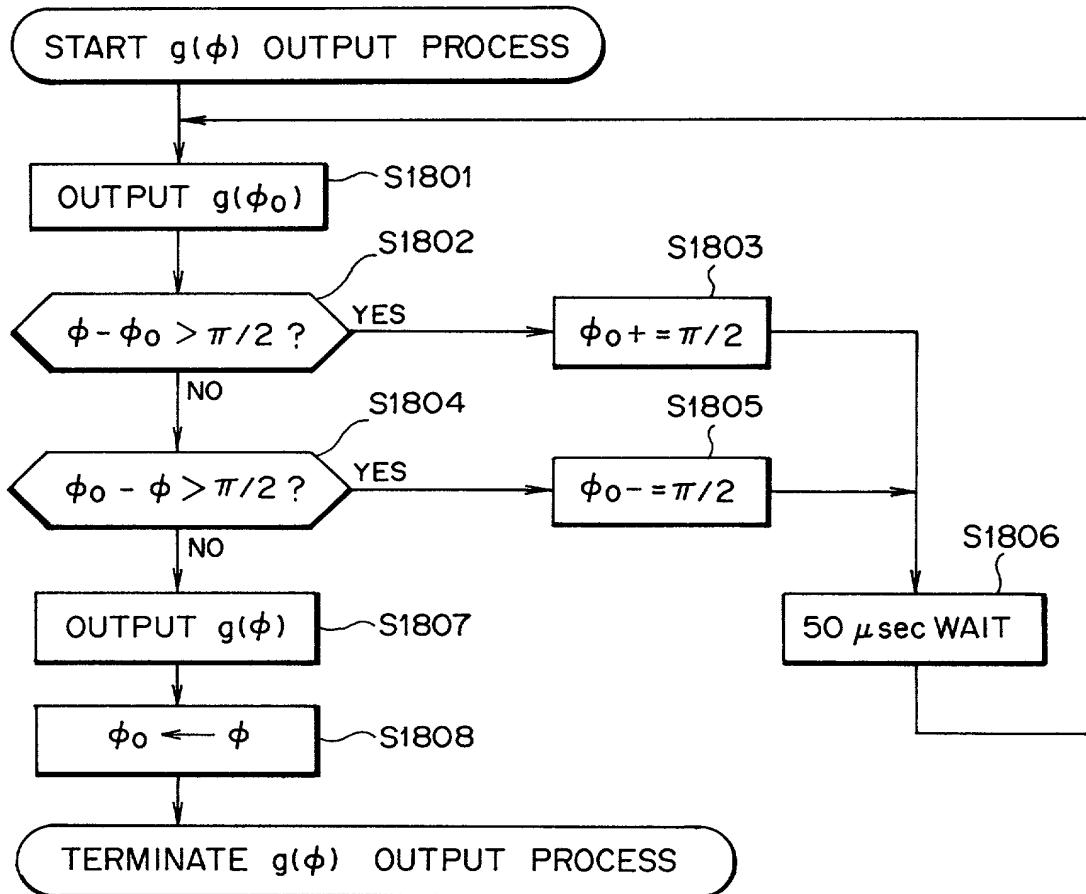


FIG.57

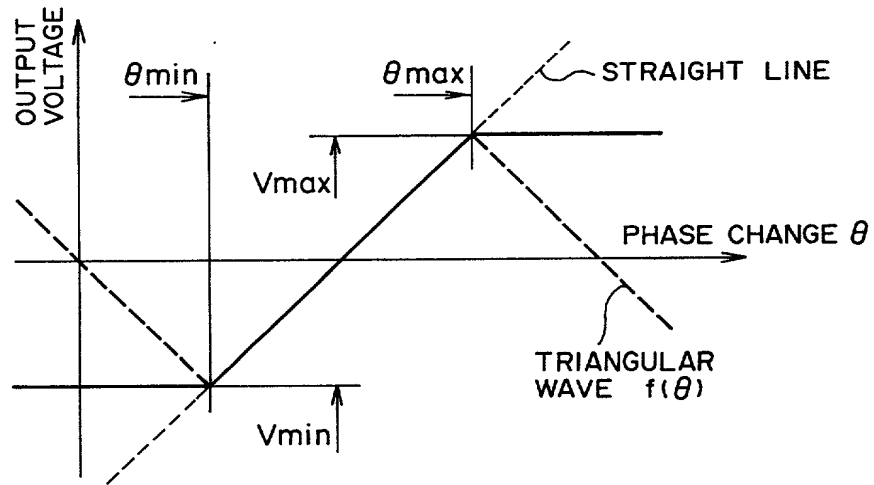


FIG.58

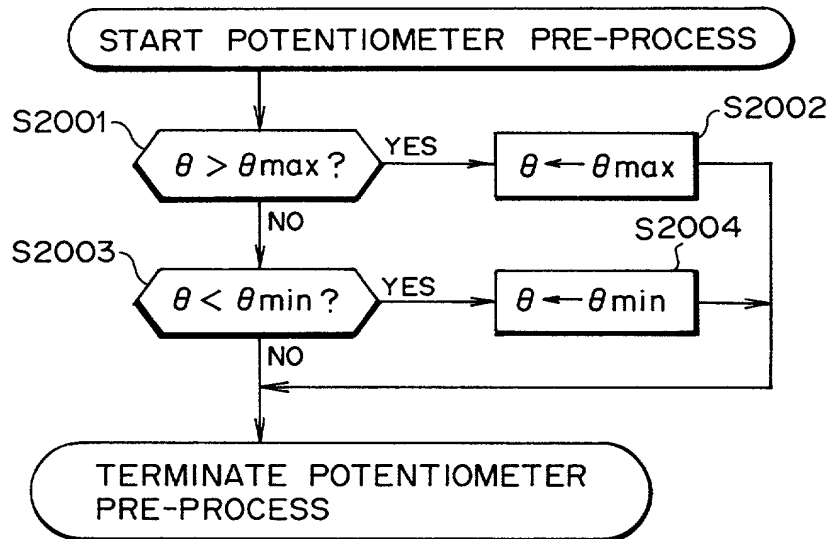


FIG. 59

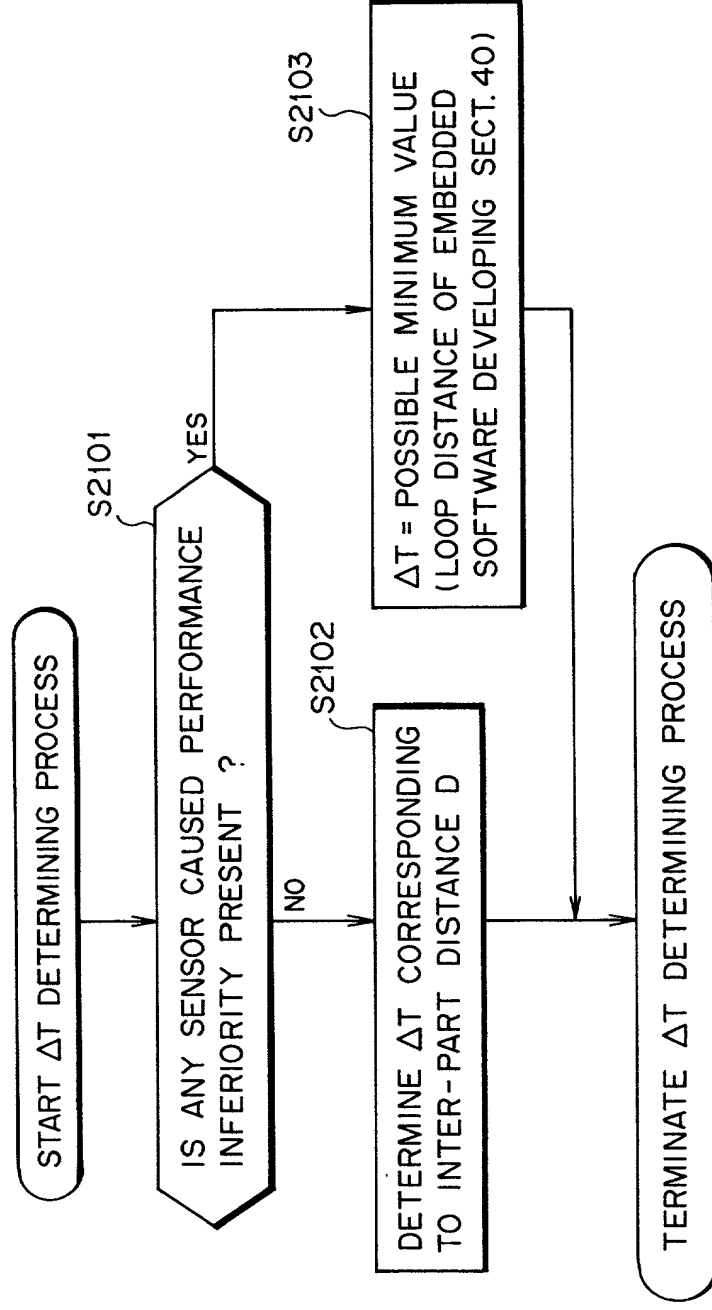


FIG. 60A

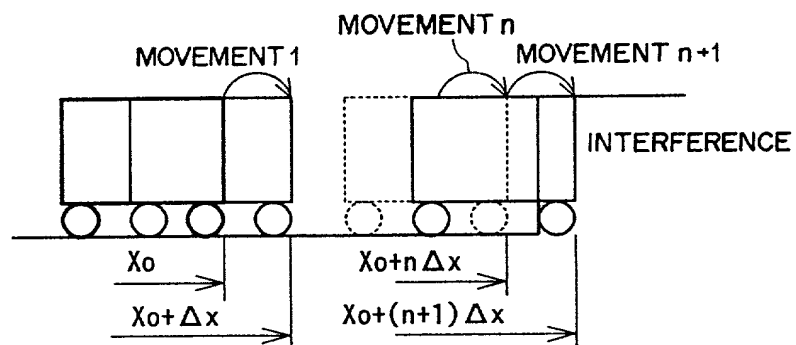


FIG. 60B

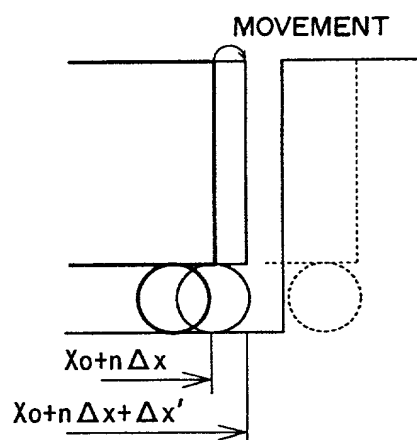


FIG. 61A

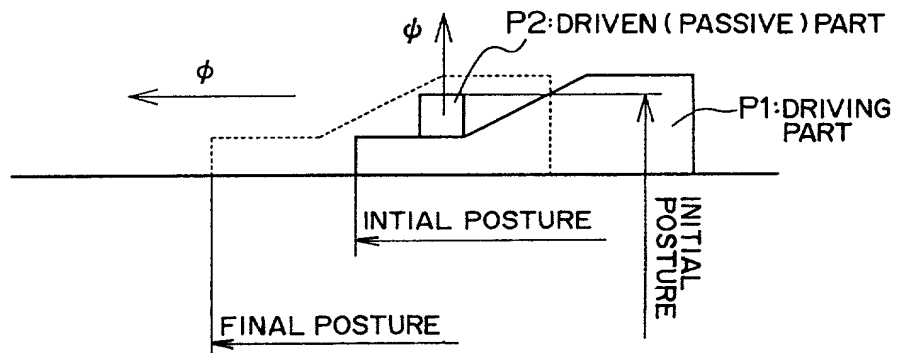


FIG. 61B

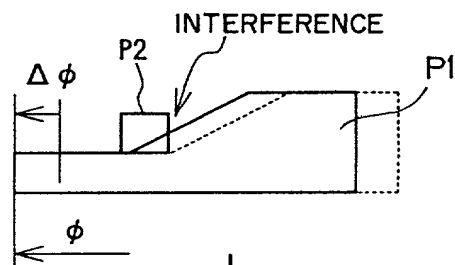


FIG. 61C

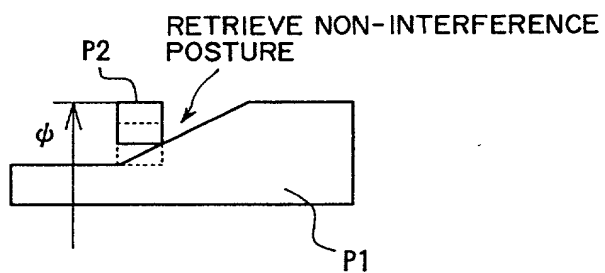


FIG. 62

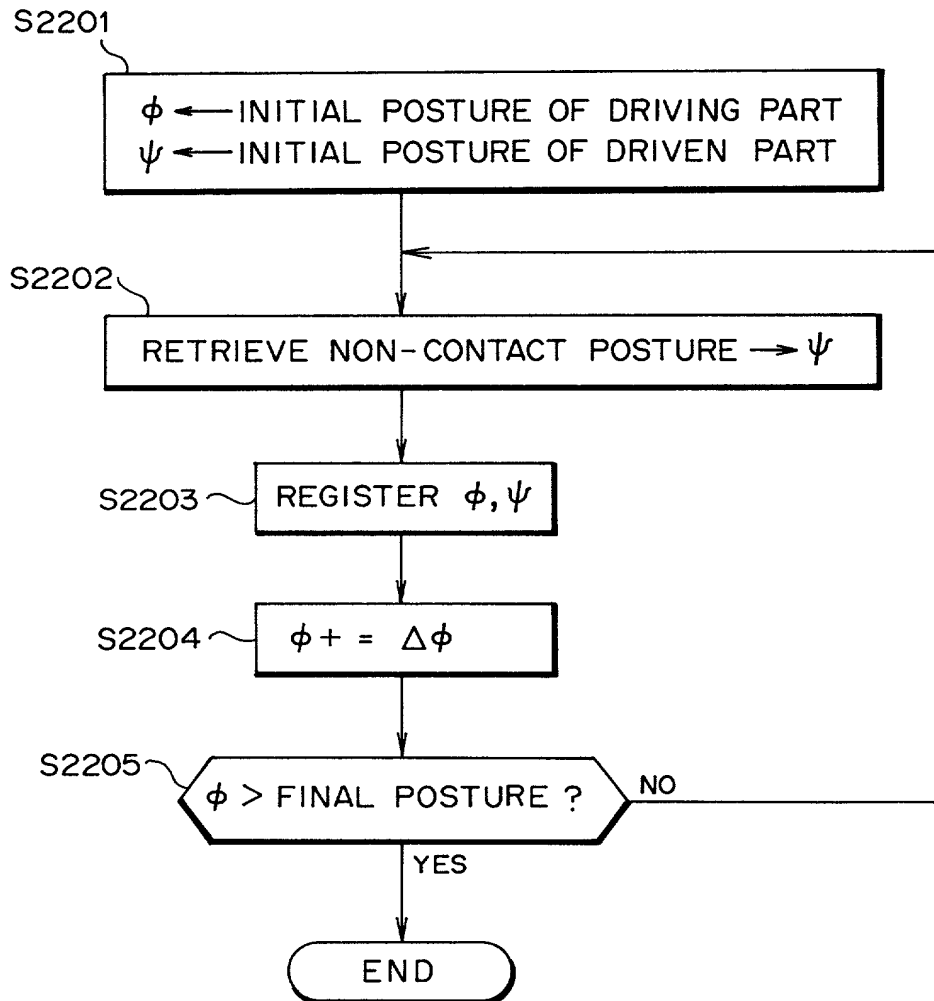


FIG. 63

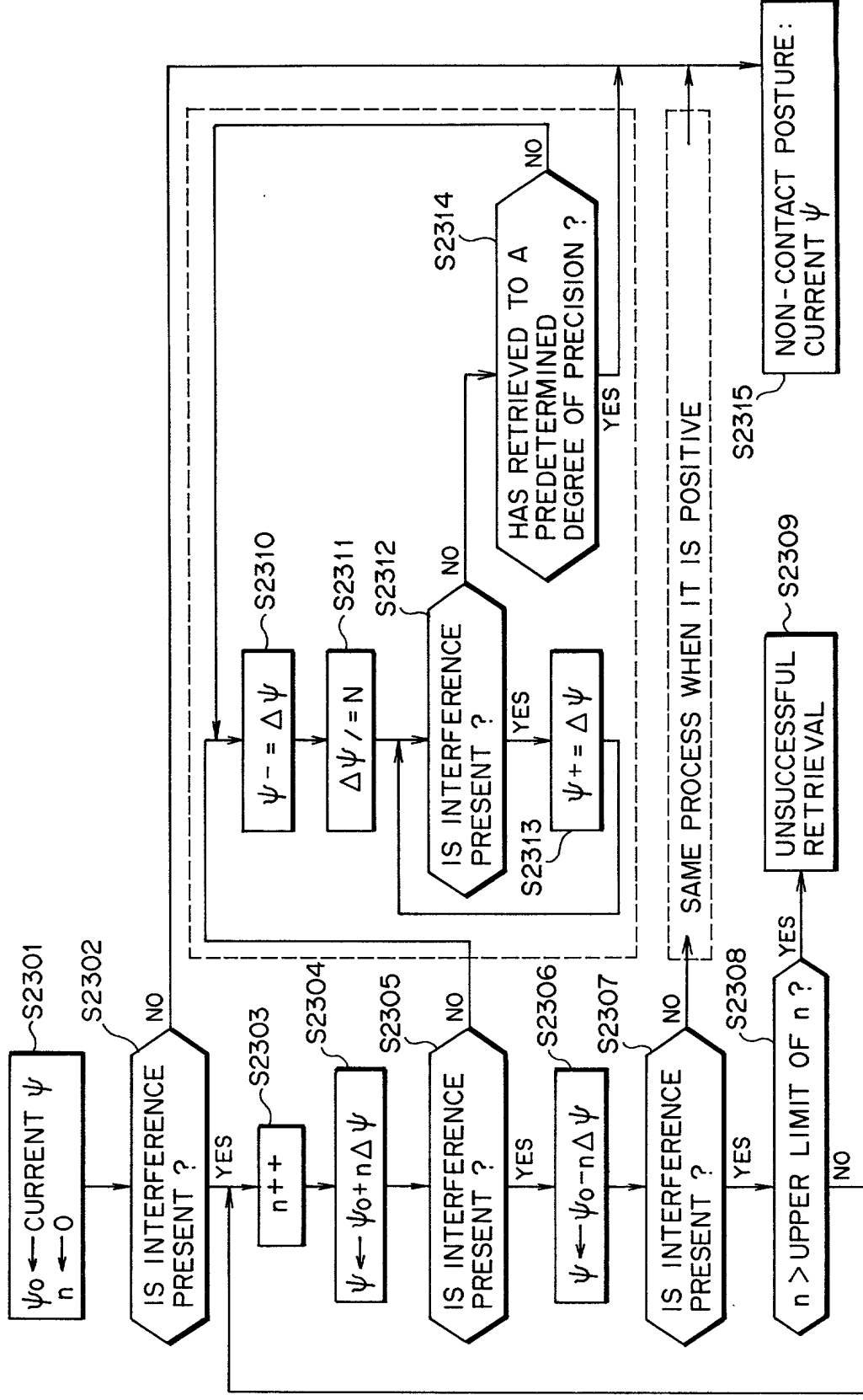


FIG. 64

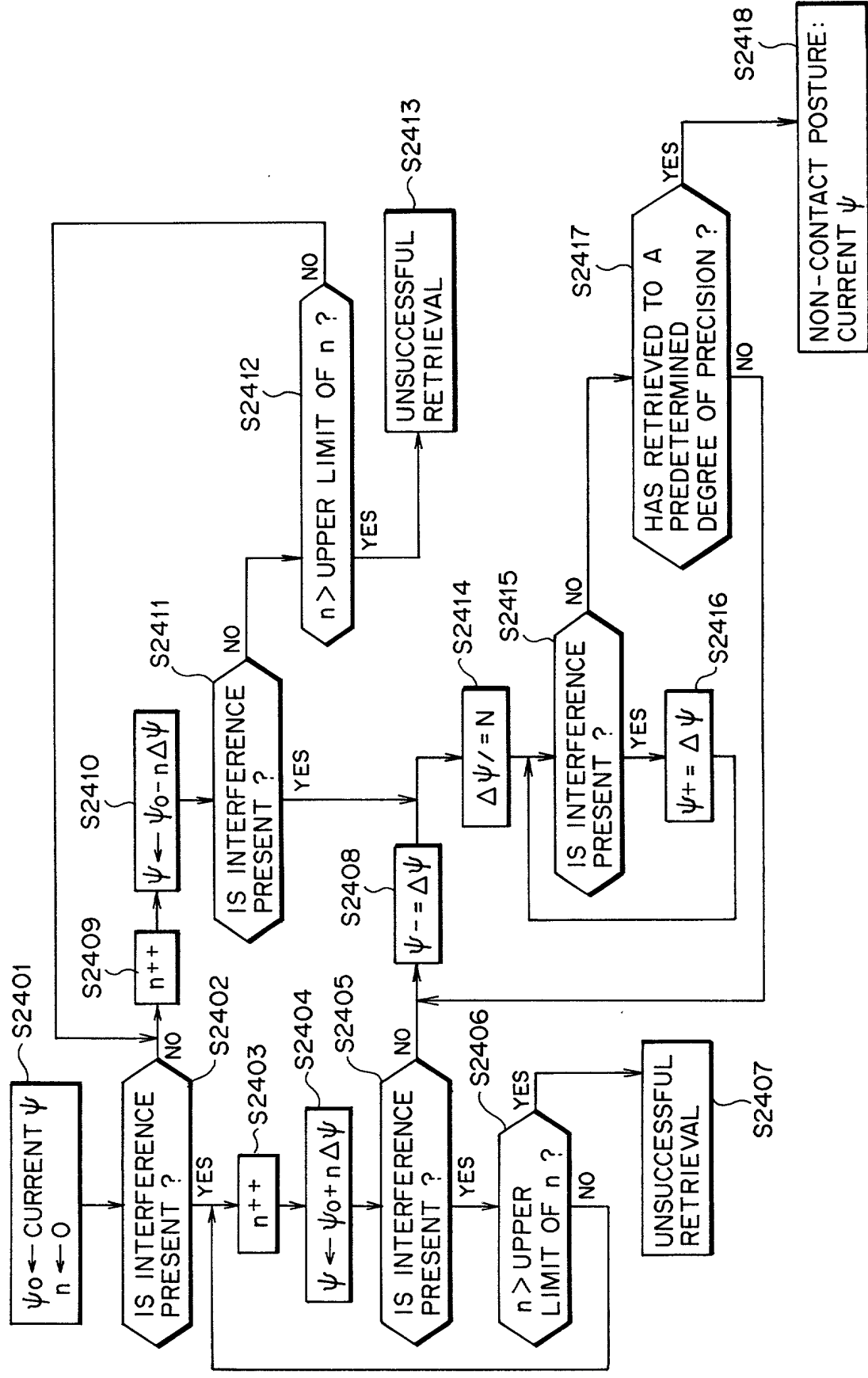


FIG. 65A

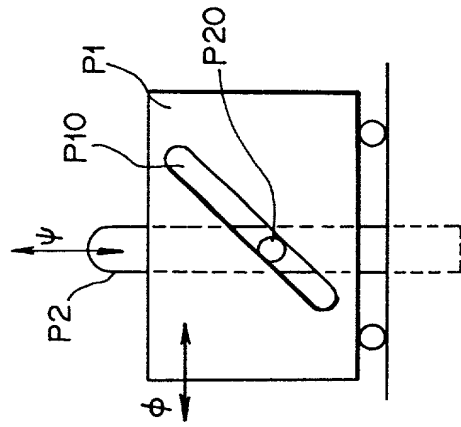


FIG. 65B

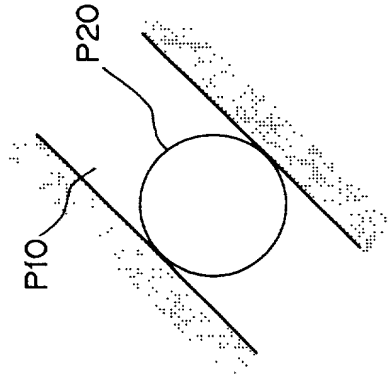


FIG. 65C

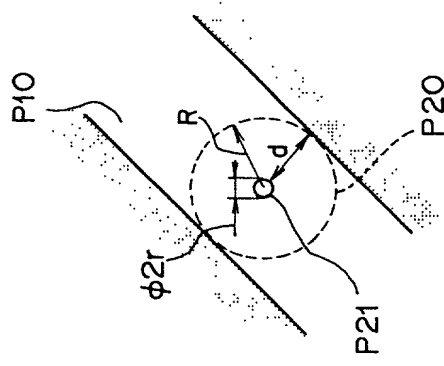


FIG. 66

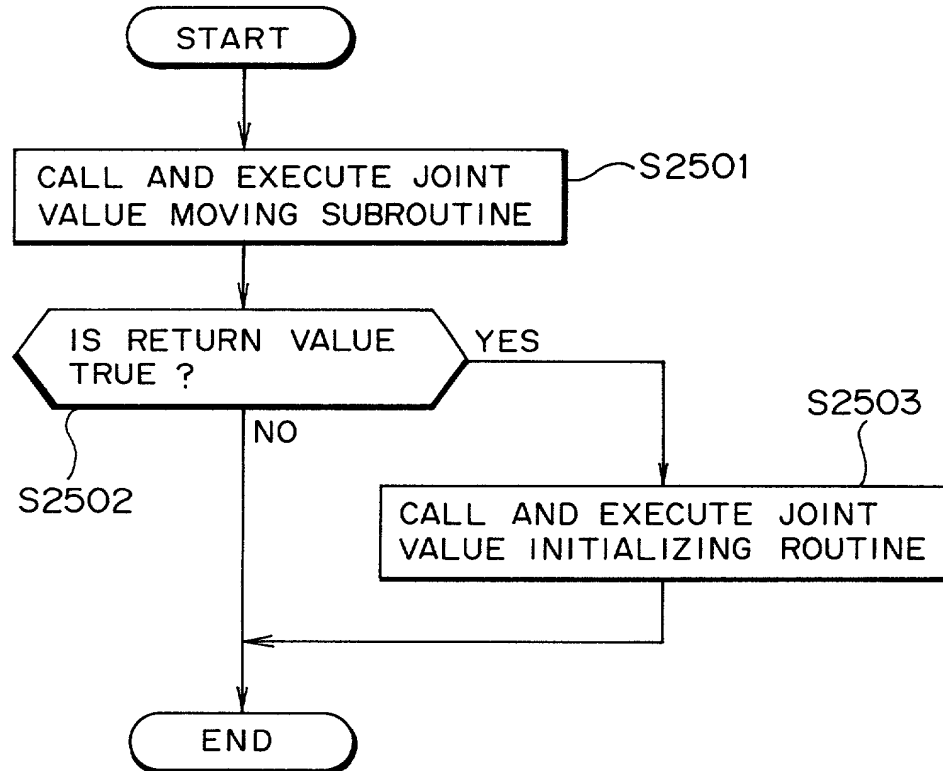


FIG. 67

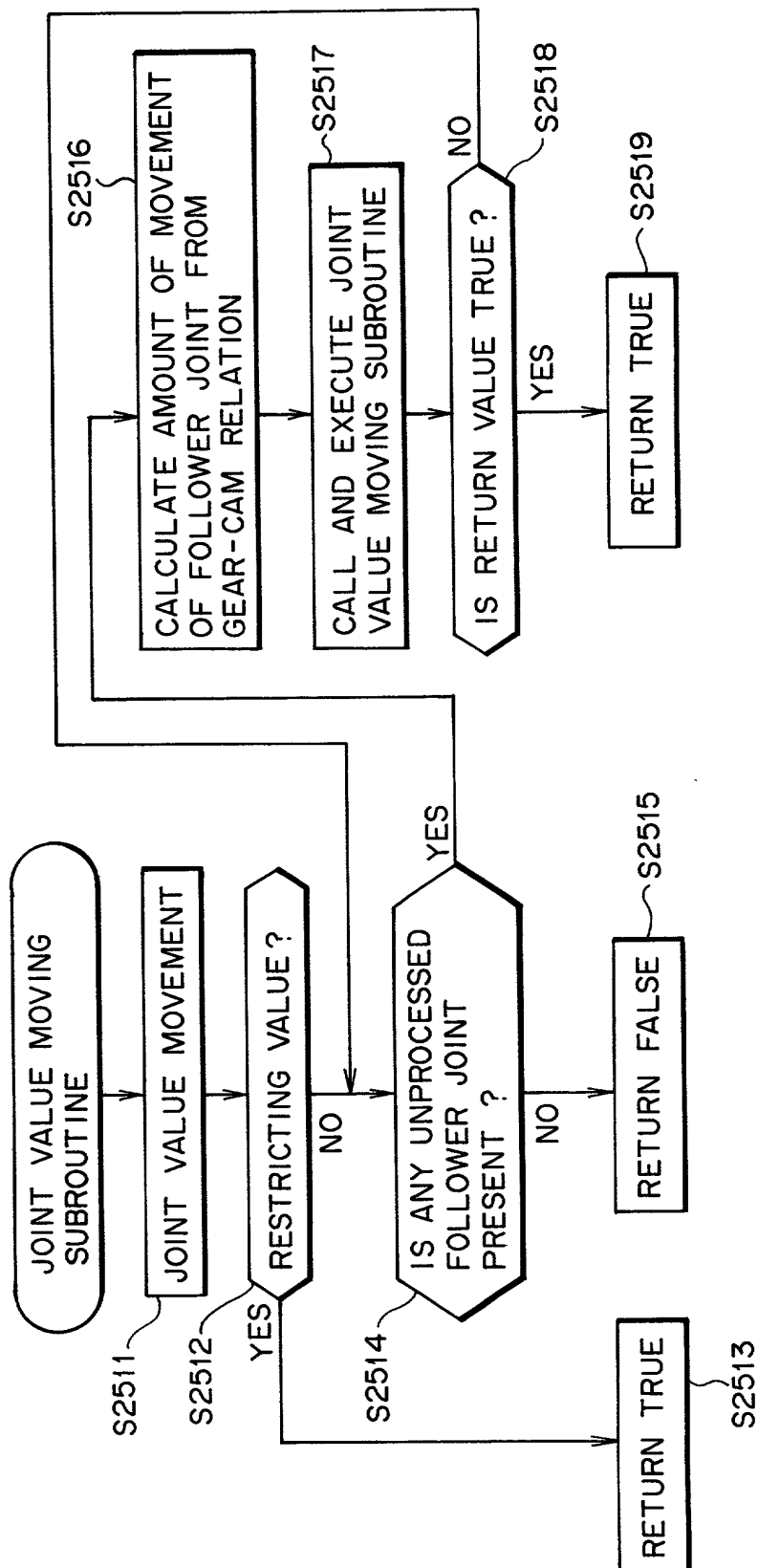


FIG. 68

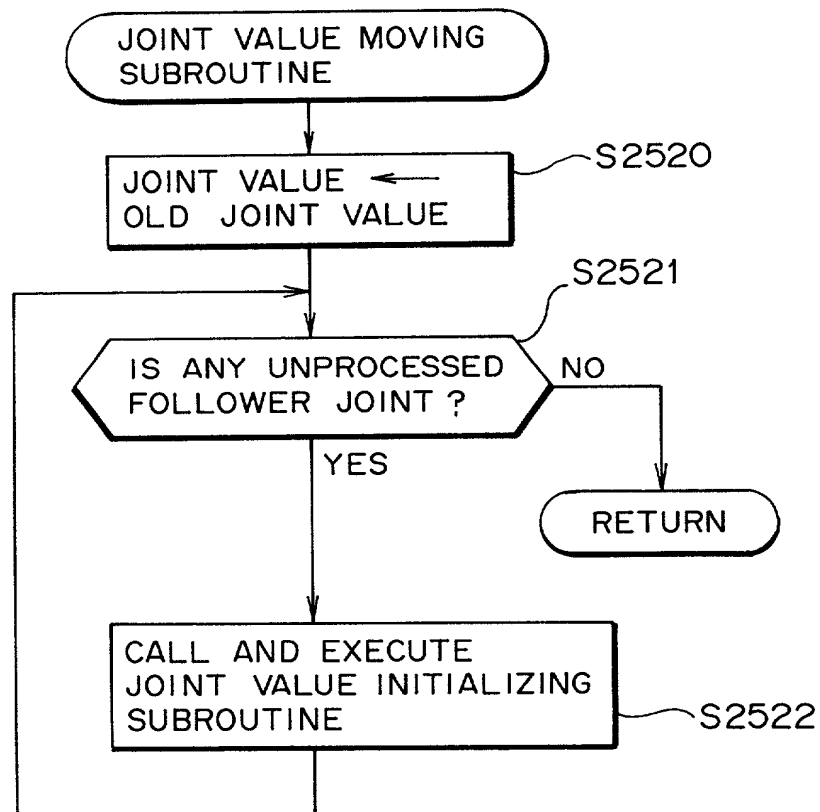


FIG. 69A

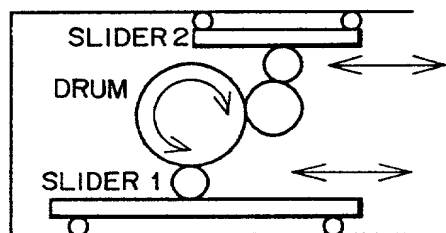


FIG. 69B

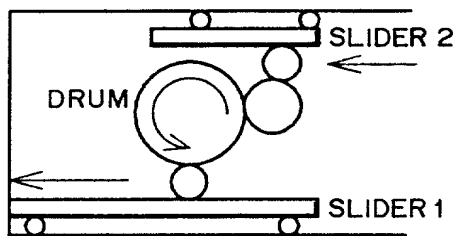


FIG. 69C

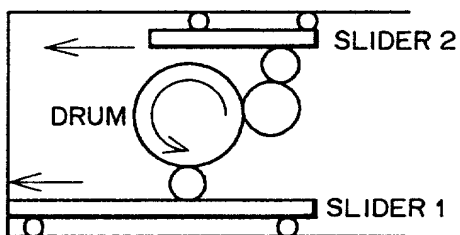


FIG.70

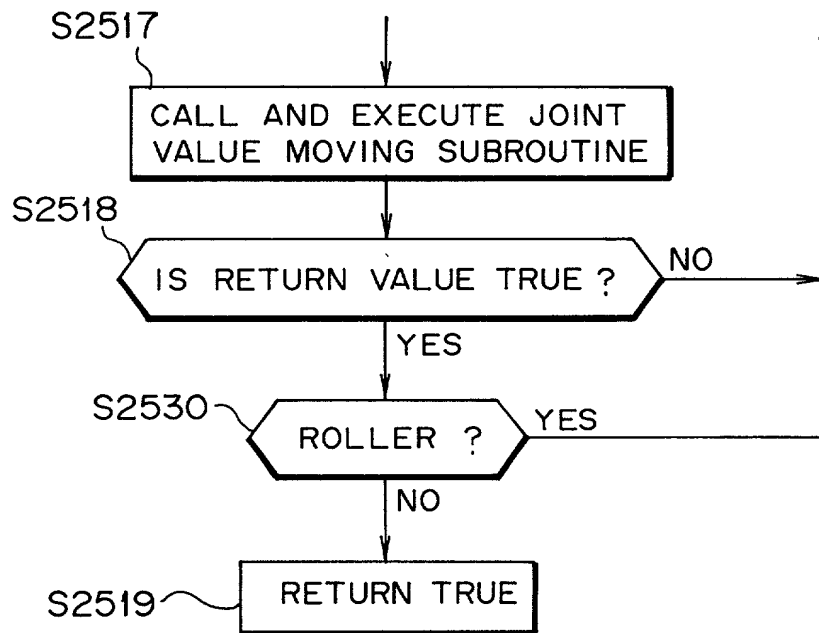


FIG. 71A

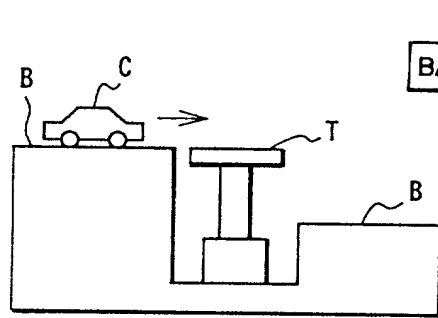


FIG. 71D

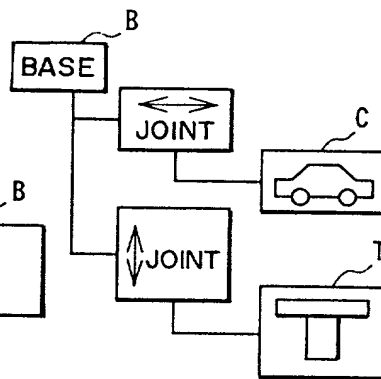


FIG. 71B

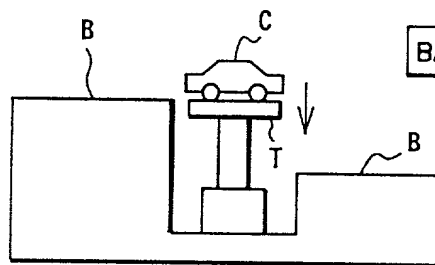


FIG. 71E

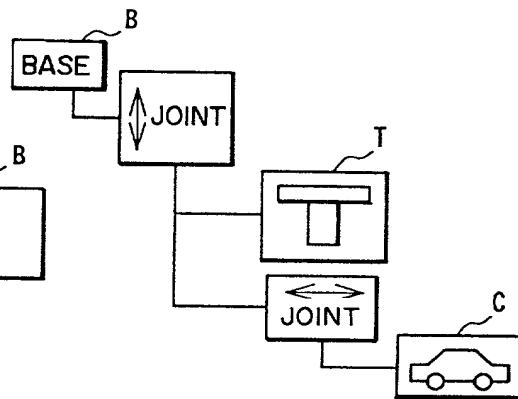


FIG. 71C

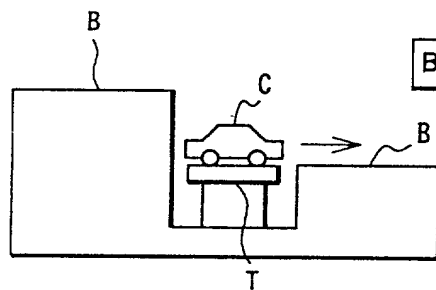


FIG. 71F

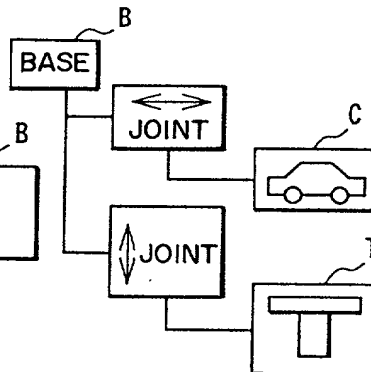


FIG. 72

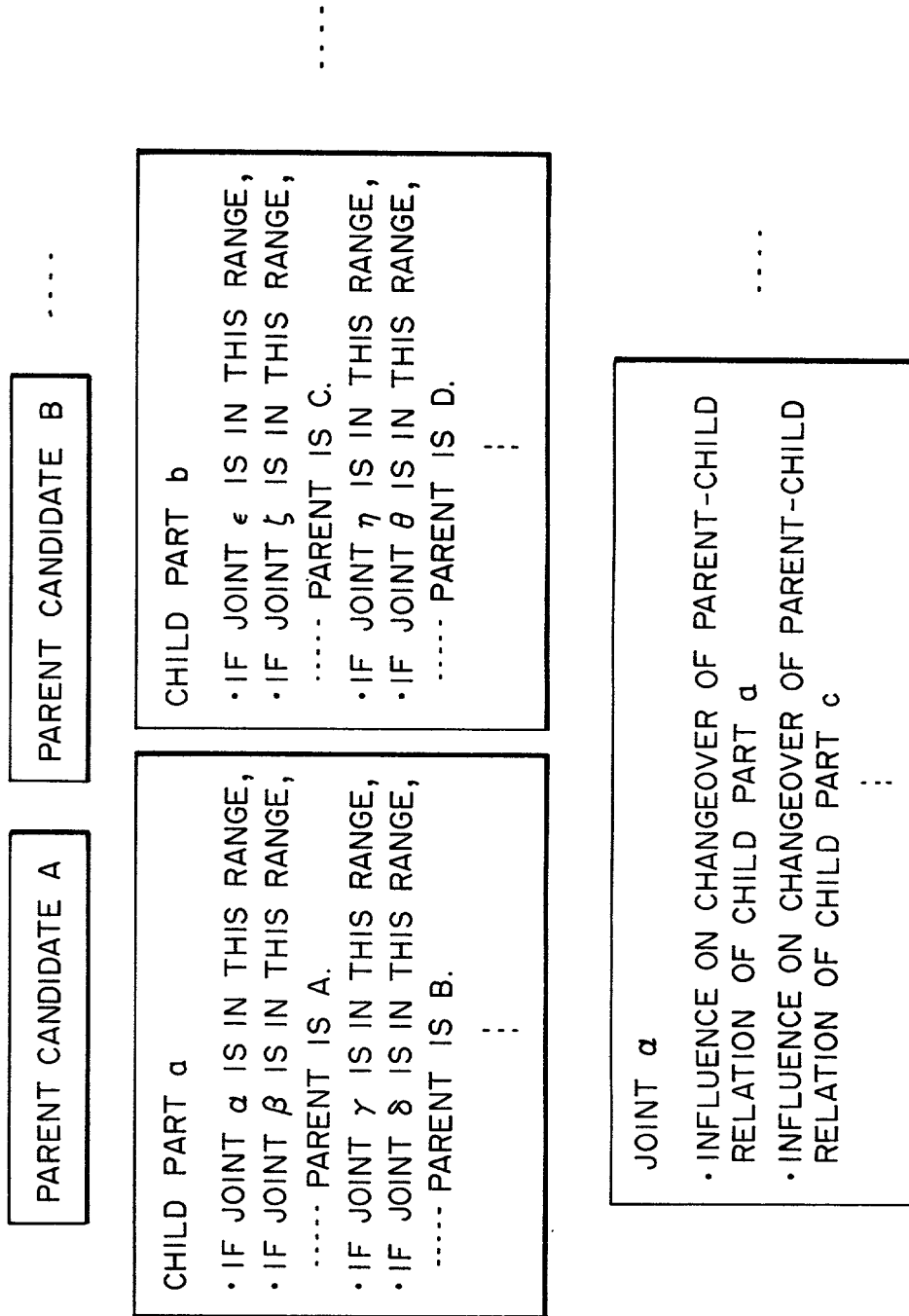


FIG. 73

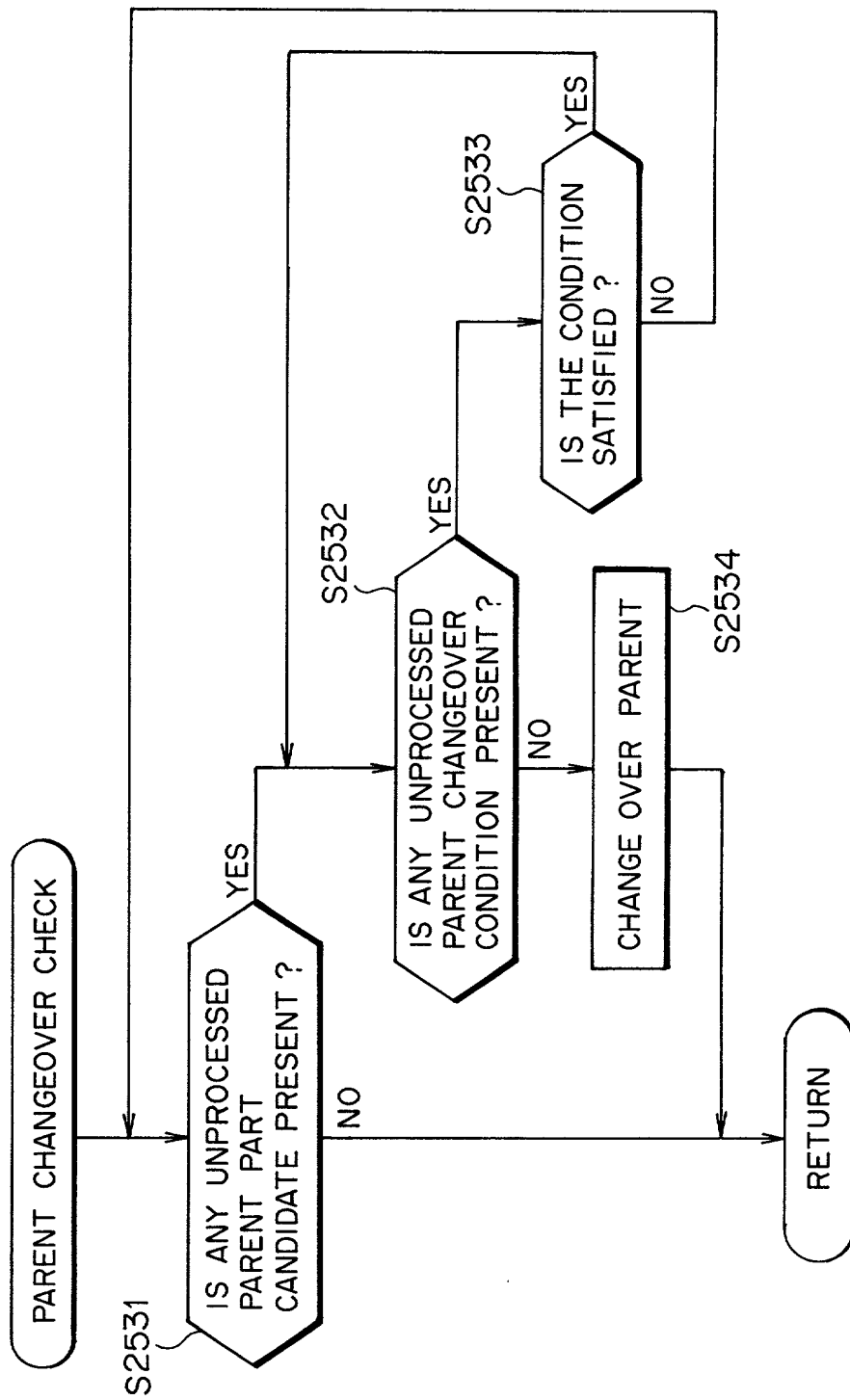


FIG.74

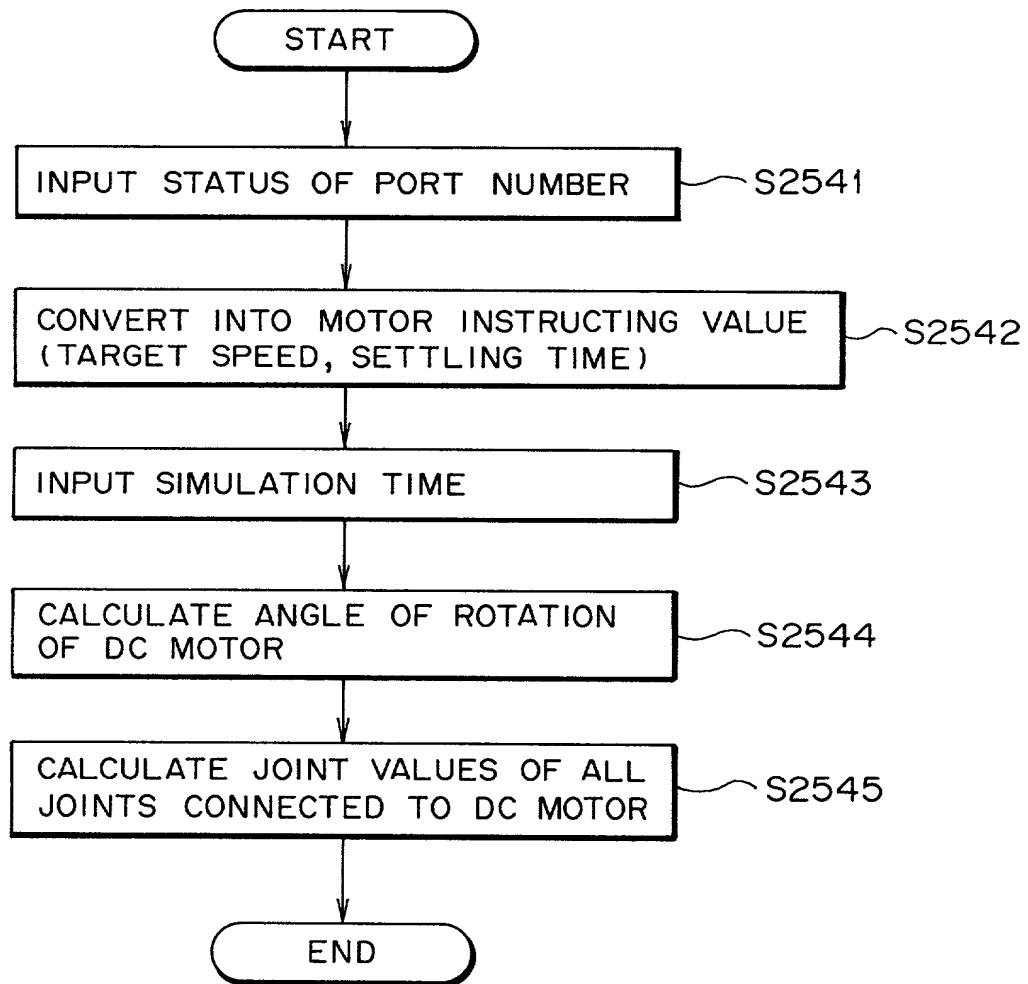


FIG. 75

MOTOR INSTRUCTIONS CORRESPONDING TO BIT STRING	PORT NUMBER		TARGET SPEED	SETTLING TIME
	11	12	[deg / s]	[ms]
	0	0	0	0
	0	1	+100	500
	1	0	-100	500
	1	1	0	0

FIG.76

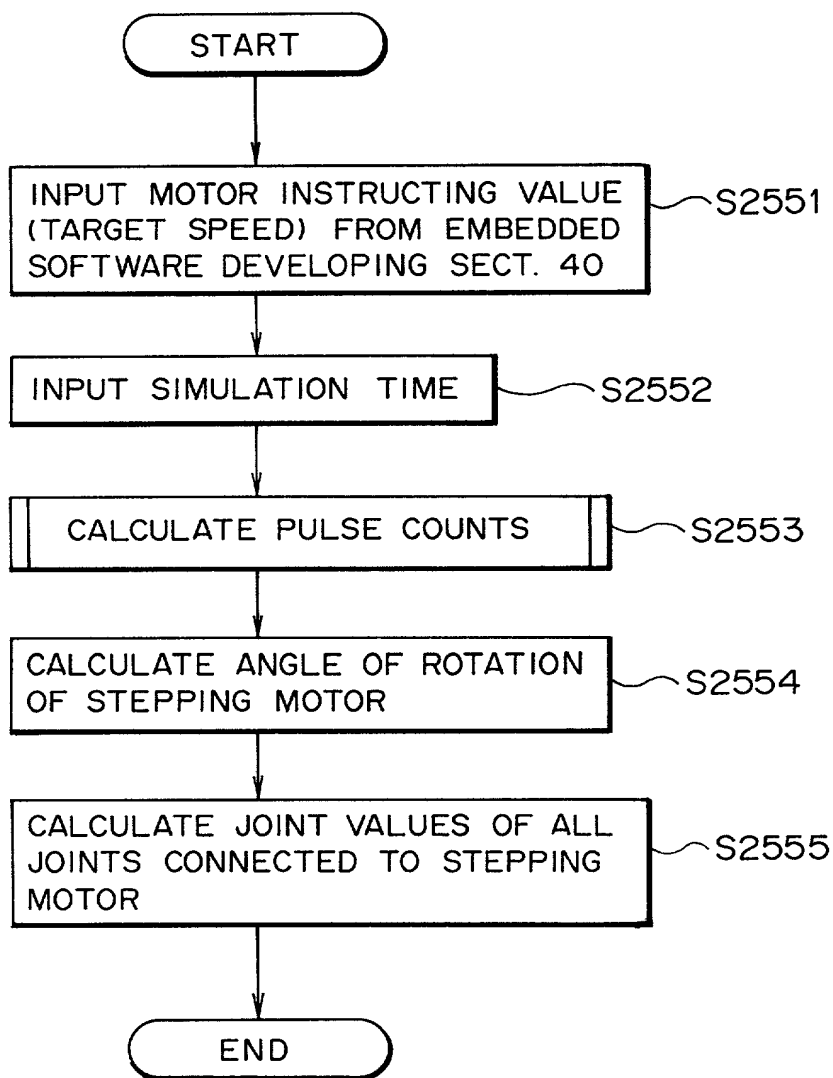
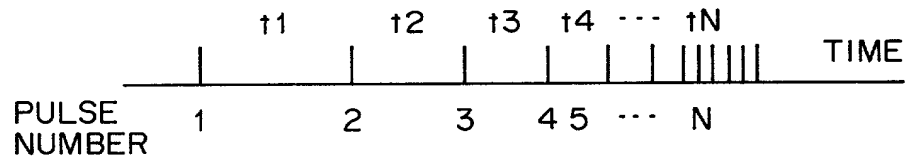


FIG.77



N = NUMBER OF ACCELERATION STEPS
 $Th[N] = \{t_1, t_2, t_3 \dots t_N\}$

FIG.78

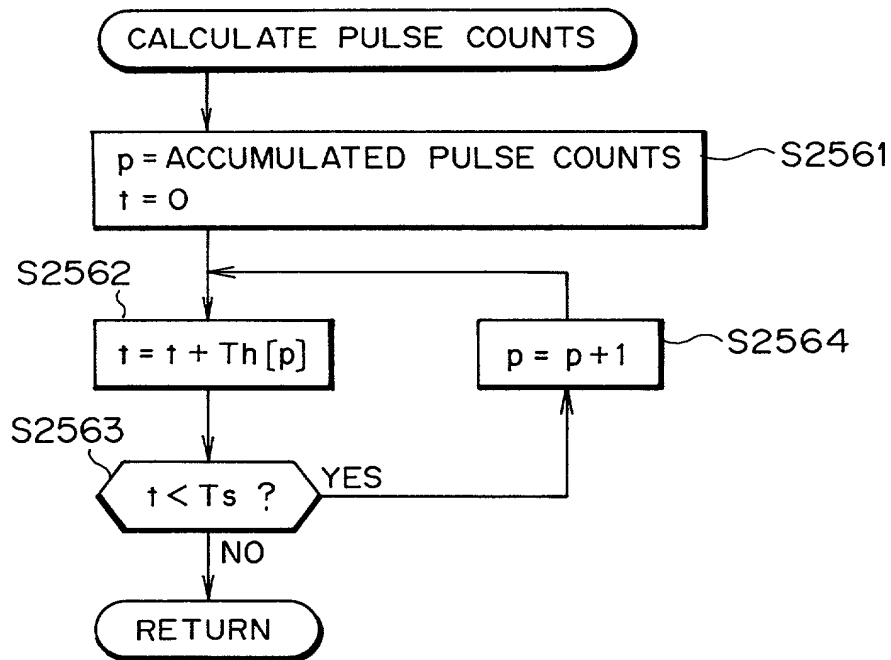


FIG.79

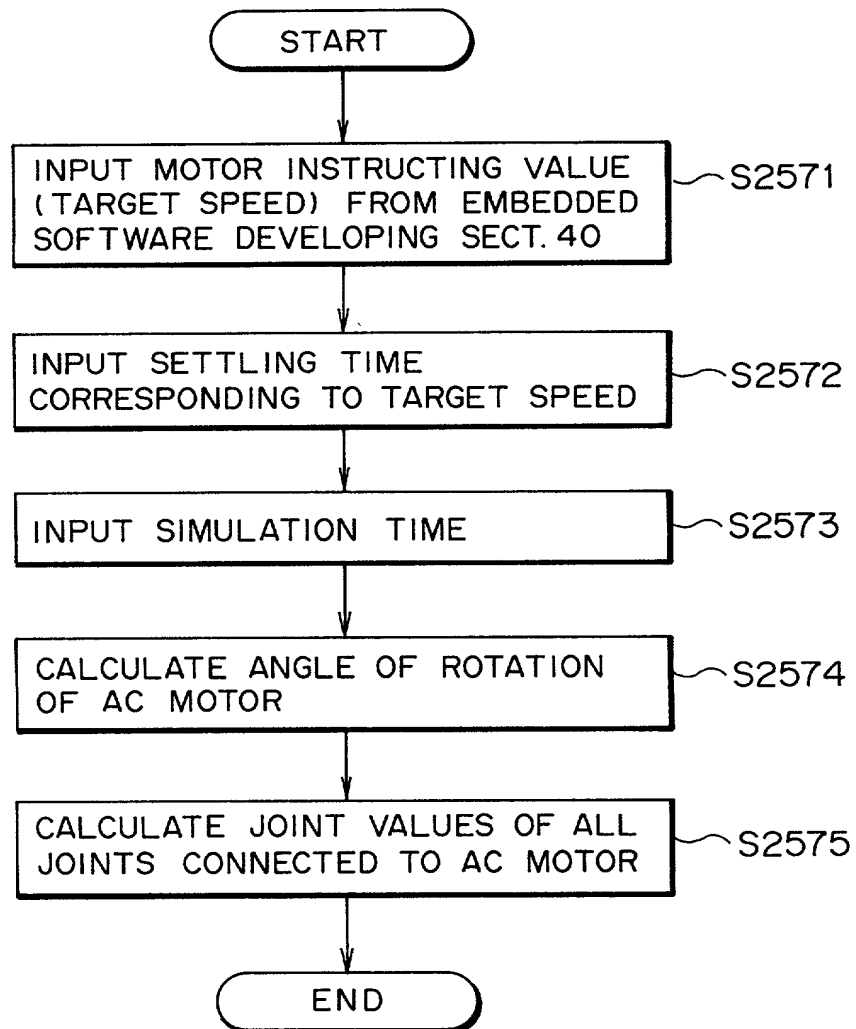


FIG.80

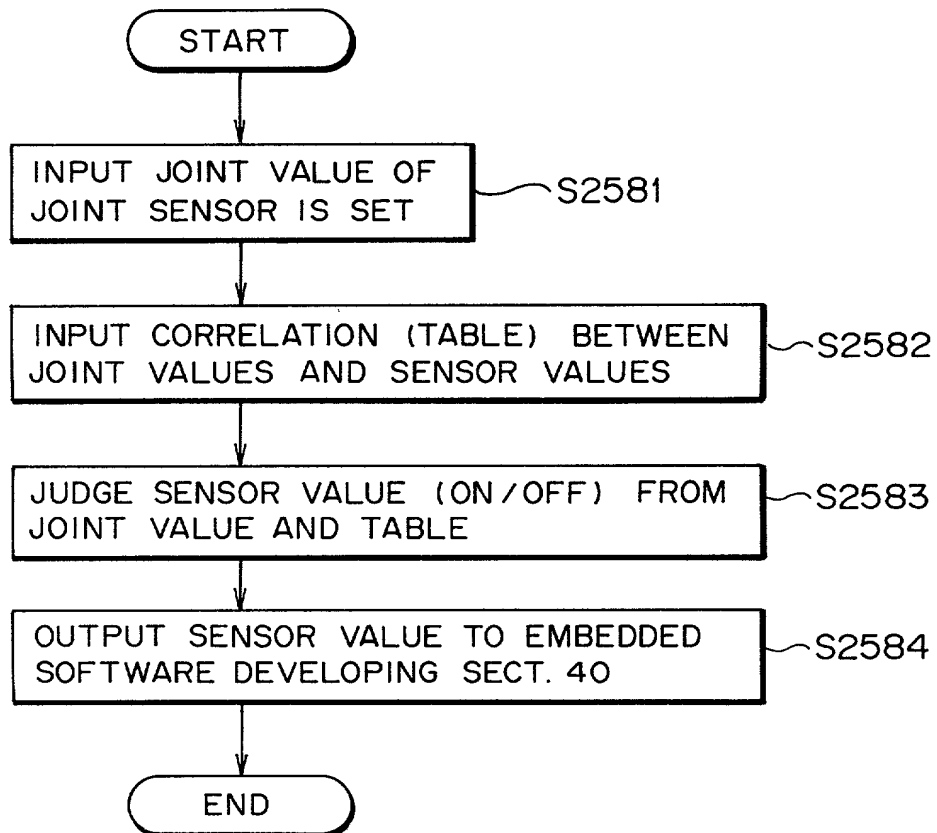


FIG. 81

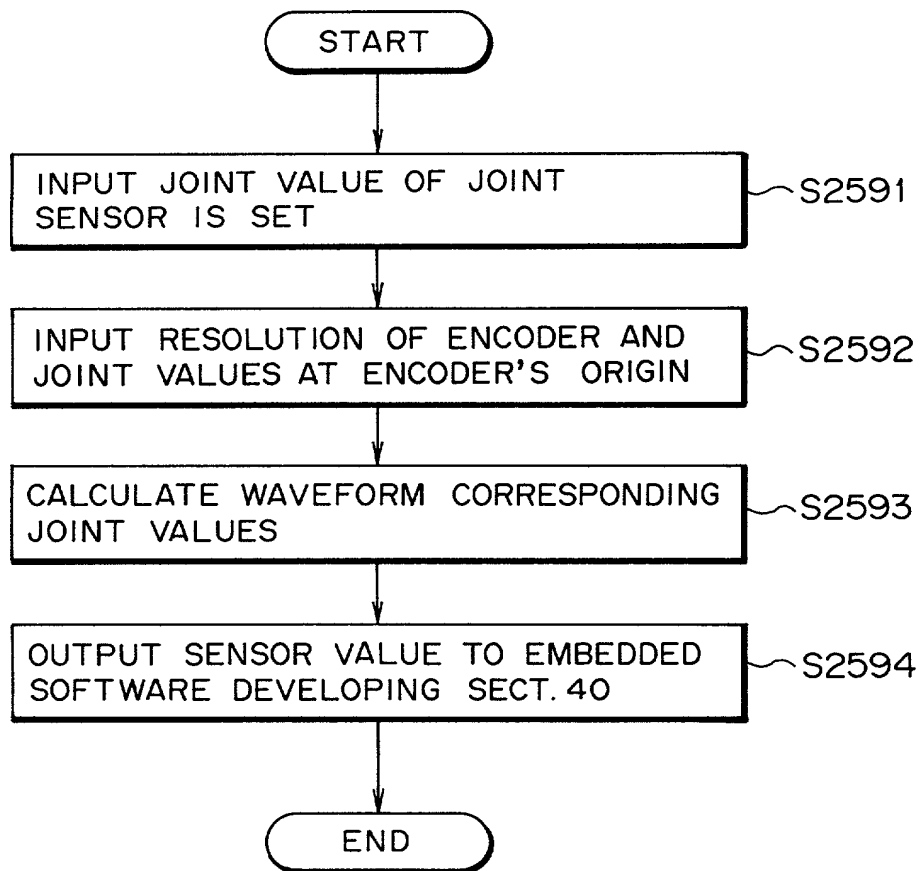


FIG. 82

